Dr. Babasaheb Ambedkar Technological University

M. Tech (EC (VLSI Design))- 12968 SEMESTER - 1 Winter 2026 (Regular/Supplementary)

University Time Table

Ref.No.DBATU/Reg-Supple Exam/M.Tech.W-26/TT/2026/5411 Date : 16/11/2026

Date & Day	Subject Name	Subject Code
07/12/2026 Monday	CORE : SIGNAL THEORY (ESE) (Time : 02:00 PM To 05:00 PM)	12968PC101
	CORE : RTL SIMULATION & SYNTHESIS WITH PLDS (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PC101
09/12/2026 Wednesday	CORE : ADVANCED DIGITAL SIGNAL PROCESSING (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PC102
	CORE : RTL SIMULATION & SYNTHESIS WITH PLDS (ESE) (Time : 02:00 PM To 05:00 PM)	12968PC103
11/12/2026 Friday	ELECTIVE : ADVANCED SIGNAL PROCESSING (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE101A
	ELECTIVE : PROGRAMMING LANGUAGE FOR EMBEDDED SYSTEMS (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE101B
	ELECTIVE : MIXED SIGNAL DESIGN (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE101C
	ELECTIVE : RF ENGINEERING (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE101D
	ELECTIVE : VLSI SIGNAL PROCESSING (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE101E
	ELECTIVE : EMBEDDED SYSTEM DESIGN (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE101F
	ELECTIVE : PARALLEL PROCESSING (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE103A
	ELECTIVE : SYSTEM DESIGN WITH EMBEDDED LINUX (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE103B
	ELECTIVE : CAD OF DIGITAL SYSTEMS (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE103C
	ELECTIVE : SMART ANTENNAS (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE103D
	ELECTIVE : ASIC DESIGN (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE103E
	ELECTIVE : VLSI DESIGN VERIFICATION & TESTING (ESE) (Time : 02:00 PM To 05:00 PM)	12968PE103F
	ELECTIVE : IMAGE PROCESSING & COMPUTER VISION (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PE103A
	ELECTIVE : PROGRAMMING LANGUAGE FOR EMBEDDED SYSTEMS (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PE103B

Printed Date : 11/6/2026
Printed Time : 11:34:48AM

Printed By : acoe_pg_engg@

Controller of Examinations
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Ref.No.DBATU/Reg-Supple Exam/M.Tech.W-26/TT/2026/5411

Date : 16/11/2026

Date & Day	Subject Name	Subject Code
11/12/2026 Friday	ELECTIVE : MIXED SIGNAL DESIGN (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PE103C
	ELECTIVE : RF ENGINEERING (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PE103D
	ELECTIVE : VLSI SIGNAL PROCESSING (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PE103E
14/12/2026 Monday	ELECTIVE : EMBEDDED SYSTEM DESIGN (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PE104A
	ELECTIVE : PARALLEL PROCESSING (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PE104B
	ELECTIVE : SYSTEM DESIGN WITH EMBEDDED LINUX (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PE104C
	ELECTIVE : CAD OF DIGITAL SYSTEMS (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PE104D
	ELECTIVE : ASIC DESIGN (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968PE104E
16/12/2026 Wednesday	ELECTIVE : RESEARCH METHODOLOGY (ESE) (Time : 02:00 PM To 05:00 PM)	24AF2968RM105

Dr.B.A. Technological University Lonere Raigad (M.S.)

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