



डॉ.बाबासाहेब आंबेडकर तंत्रशास्त्र विद्यापीठ,लोणेरे

Dr. Babasaheb Ambedkar Technological University, Lonere

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परीक्षा विभाग

Dr. Narendra S. Jadhav

Controller of Examination(I/c)

डॉ. नरेंद्र सु. जाधव

परीक्षा नियंत्रक(प्र.)

Ref no: DBATU/Reg.Exam/B.Tech S-25/TT/2025/4032

Date: 21/06/2025

B.Tech Time Table Regular Summer - 2025 (Only for University Department 3033)

VLSI Design & Technology (11378)

Sr. No.	Date	Time	Course Name	Subject Code	Subject Name
1	01/07/2025	10:00 AM To 01:00 PM	VLSI Design & Technology	24UD1378PC401	Electronic Devices & Circuits
2	03/07/2025	10:00 AM To 01:00 PM	VLSI Design & Technology	24UD1378PC403	System Design through Verilog
3	07/07/2025	10:00 AM To 01:00 PM	VLSI Design & Technology	24UD1000HM409	Patents and IPR
4	09/07/2025	10:00 AM To 01:00 PM	VLSI Design & Technology	24UD1372VS411	PCB Designing
5	11/07/2025	10:00 AM To 01:00 PM	VLSI Design & Technology	24UD1MACOEM05N	Fundamentals of Financial Management
				24UD1HAROEM05I	Gender & Violence
				24UD1SCIOEM05I	Algebra I
6	14/07/2025	10:00 AM To 01:00 PM	VLSI Design & Technology	24UD1372MD406	Signals and Systems
7	16/07/2025	10:00 AM To 01:00 PM	VLSI Design & Technology	24UD1378PC412	Microprocessor
8	18/07/2025	10:00 AM To 01:00 PM	VLSI Design & Technology	24UD1000AE410A	Modern Indian Language (Marathi)
				24UD1000AE410B	Modern Indian Language (Hindi)
				24UD1000AE410C	Modern Indian Language (Sanskrit)

Important Note: 1.The candidates appearing for the examination should report 20 minutes before the start of examination.

2. Mobile phones and other electronic gadgets are prohibited in the examination hall.

3.Change if any, in the time table shall be communicated on the University web site.

Copy to:

1. Hon 'ble Vice Chancellor (For Information)

2.The Registrar (For Information)

3.All HoDs

4.Students Notice Board

5.University Website- www.dbatu.ac.in

Controller of Examinations(I/C)

**Dr. Babasaheb Ambedkar Technological
University, Lonere**