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 परीक्षा विभाग



Dr. Narendra S. Jadhav
Controller of Examination(I/c)

Ref. No.DBATU/Reg. & Suppli EXAM/M. Tech W-24/ TT/ 2025/3453

डॉ. नरेंद्र स. जाधव
 परीक्षा नियंत्रक (प्र.)
 Date: 23-04-2025

M.Tech Time Table Regular Winter -2024
Electronics & Allied Course (12969/12372/12968)

Sr.No	Date	Time	Course	Subject Code	Subject Name
1	02-05-2025	2.00 PM to 5.00 PM	M. Tech (Electronics & Communication (Advanced Communication Technology))	12969PC101	Signal Theory
			M. Tech (Electronics & Telecommunication)	12372PC101	Signal Theory
			M. Tech (Electronics & Communication (VLSI Design))	12968PC101	Signal Theory
2	05-05-2025	2.00 PM to 5.00 PM	M. Tech (Electronics & Communication (Advanced Communication Technology))	12969PC103	5G Technology-1
			M. Tech (Electronics & Telecommunication)	12372PC103	Radiation and Microwave Techniques
			M. Tech (Electronics & Communication (VLSI Design))	12968PC103	RTL Simulation & Synthesis with PLDs
3	07-05-2025	2.00 PM to 5.00 PM	M. Tech (Electronics & Communication (Advanced Communication Technology))	12969PE101G	Digital Communication Techniques
			M. Tech (Electronics & Telecommunication)	12372PE101B	Electromagnetic Interference and Compatibility
			M. Tech (Electronics & Communication (VLSI Design))	12968PE101B	Programming Language for Embedded Systems
4	09-05-2025	2.00 PM to 5.00 PM	M. Tech (Electronics & Communication (Advanced Communication Technology))	12969PE103E	Advanced Digital Signal Processing
			M. Tech (Electronics & Telecommunication)	12372PE103A	RF and Millimeter Wave circuit Design
			M. Tech (Electronics & Communication (VLSI Design))	12968PE103	CAD of Digital Systems

Important Note: 1.The candidates appearing for the examination should report 20 minutes before the start of examination.
 2. Mobile phones and other electronic gadgets are prohibited in the examination hall.

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N. Jadhav
 23/4/25

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