



डॉ.बाबासाहेब आंबेडकर तंत्रशास्त्र विद्यापीठ,लोणेरे
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परीक्षा विभाग

Dr. Narender S. Jadhav
Controller of Examination(I/c)

डॉ. नरेंद्र सु. जाधव
परीक्षा निबंधक (प्र.)

Ref. No.DBATU/Suppli EXAM/M. Tech W-24/ TT/ 2025/3453

Date: 23-04-2025

M.Tech Time Table Supplementary Winter-2024
Electronics & Allied Course (12372/12370/12968/12969/12341)

Sr.No.	Date	Time	Course Name	Subject Code	Subject Name
1	03-05-2025	2.00 PM to 5.00 PM	M. Tech(Electronics and Telecommunication)	MTETC201	Estimation and Detection Theory
			M. Tech (Electronics and Communication Engineering)	MTECC201	Image and Video Processing
			M.Tech(Electronics and Communication (VLSI Design))	MTVLSIC201	Analog & Digital VLSI Design
			M. Tech(Electronics and Communication (Advanced Communication Technology))	MTACTC201	5G Technology-2
			M. Tech (VLSI & Embedded Systems)	MTVEC201	Embedded Real Time Operating Systems
2	06-05-2025	2.00 PM to 5.00 PM	M. Tech(Electronics and Telecommunication)	MTETC202	Information Theory and Coding
			M. Tech (Electronics and Communication Engineering)	MTECC202	Information Theory and Coding
			M.Tech(Electronics and Communication (VLSI Design))	MTVLSIC202	VLSI Design Verification & Testing
			M. Tech(Electronics and Communication (Advanced Communication Technology))	MTACTC202	5G-Mobile Communication
			M. Tech (VLSI & Embedded Systems)	MTVEC202	CMOS Mixed Signal Circuit Design
3	08-05-2025	2.00 PM to 5.00 PM	M. Tech(Electronics and Telecommunication)	MTETE233A	Multirate Digital Signal Processing
			M. Tech(Electronics and Telecommunication)	MTETE233B	Embedded System Design
			M. Tech(Electronics and Telecommunication)	MTETE233C	Wireless Sensor Network Design
			M. Tech (Electronics and Communication Engineering)	MTECE233A	Multirate Digital Signal Processing
			M.Tech(Electronics and Communication (VLSI Design))	MTVLSIE233D	Computer Networks
			M.Tech(Electronics and Communication (VLSI Design))	MTVLSIE233E	Real Time Embedded Systems
			M. Tech(Electronics and Communication (Advanced Communication Technology))	MTACTE233C	Cryptography & Network Security
			M. Tech (VLSI & Embedded Systems)	MTVEE233B	Digital Signal Processors and Architectures
4	14-05-2025	2.00 PM to 5.00 PM	M. Tech(Electronics and Telecommunication)	MTETE244A	Advanced Biomedical Signal Processing
			M. Tech(Electronics and Telecommunication)	MTETE244C	Digital VLSI Design
			M. Tech(Electronics and Telecommunication)	MTETE244D	Radar Signal Processing
			M. Tech(Electronics and Telecommunication)	MTETE244E	Electromagnetics,Antenna and Propagation
			M. Tech (Electronics and Communication Engineering)	MTECE244E	Multicarrier Communication
			M.Tech(Electronics and Communication (VLSI Design))	MTVLSIE244A	Network Security & Cryptography

Sr.No.	Date	Time	Course Name	Subject Code	Subject Name
5	16-05-2025	2.00 PM to 5.00 PM	M.Tech(Electronics and Communication (VLSI Design))	MTVLSIE244B	Physical Design Automation
			M. Tech(Electronics and Communication (Advanced Communication Technology))	MTACTE244A	Cloud Computing
			M. Tech (VLSI & Embedded Systems)	MTVEE244A	Sensors and Actuators
			M. Tech(Electronics and Telecommunication)	MTETE255A	Internet of Things
			M. Tech(Electronics and Telecommunication)	MTETE255D	Research Methodology
			M. Tech (Electronics and Communication Engineering)	MTECE255C	Multimedia Communication
			M.Tech(Electronics and Communication (VLSI Design))	MTVLSIE255A	Artificial Intelligence & Machine Learning
			M.Tech(Electronics and Communication (VLSI Design))	MTVLSIE255E	Industrial Safety
			M. Tech(Electronics and Communication (Advanced Communication Technology))	MTACTE255A	Artificial Intelligence & Machine learning
			M. Tech (VLSI & Embedded Systems)	MTVEE255A	Internet of Things

Important Note: 1.The candidates appearing for the examination should report 20 minutes before the start of examination.
2. Mobile phones and other electronic gadgets are prohibited in the examination hall.

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- 2.The Registrar (For Information)
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- 6.University Website- www.dbatu.ac.in



N. B. Ambedkar
23/04/24

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