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परीक्षा विभाग



Dr. Narendra S. Jadhav  
Controller of Examination(I/c)

डॉ. नरेंद्र सु. जाधव  
परीक्षा निबंधक (प्र.)  
Date: 08-07-2024

Ref. No.DBATU/Suppli EXAM/M. Tech S-24/ TT/ 2024/2772

**M.Tech Time Table Supplementary Summer-2024**  
**EXTC & Allied (12372/12968/12341)**


Sr.No.	Date	Time	Course Name	Subject Code	Subject Name
1	22-07-2024	2.00 PM to 5.00 PM	M. Tech (Electronics and Telecommunication Engineering)	MTETC101	Signal Theory
2	22-07-2024	2.00 PM to 5.00 PM	M.Tech Electronics and Communication (VLSI Design)	MTVLSIC101	RTL Simulation & Synthesis with PLDs
3	22-07-2024	2.00 PM to 5.00 PM	M. Tech (VLSI & Embedded Systems)	MTVEC101	VLSI Technology and Design
4	24-07-2024	2.00 PM to 5.00 PM	M. Tech (Electronics and Telecommunication Engineering)	MTETC102	Radiation and Microwave Techniques
5	24-07-2024	2.00 PM to 5.00 PM	M.Tech Electronics and Communication (VLSI Design)	MTVLSIC102	Embedded System
6	24-07-2024	2.00 PM to 5.00 PM	M. Tech (VLSI & Embedded Systems)	MTVEC102	CMOS Analog Circuit Design
7	26-07-2024	2.00 PM to 5.00 PM	M. Tech (Electronics and Telecommunication Engineering)	MTETC103	Signal Processing Algorithms and Applications
8	26-07-2024	2.00 PM to 5.00 PM	M.Tech Electronics and Communication (VLSI Design)	MTVLSIC103	Advance Digital Signal Processing
9	26-07-2024	2.00 PM to 5.00 PM	M. Tech (VLSI & Embedded Systems)	MTVEC103	Advanced Embedded Logic
10	29-07-2024	2.00 PM to 5.00 PM	M. Tech (Electronics and Telecommunication Engineering)	MTETE114C	Mobile Communication
11	29-07-2024	2.00 PM to 5.00 PM	M.Tech Electronics and Communication (VLSI Design)	MTVLSIE114A	Image Processing & Computer Vision
12	29-07-2024	2.00 PM to 5.00 PM	M. Tech (VLSI & Embedded Systems)	MTVEE114B	Digital System Design
13	31-07-2024	2.00 PM to 5.00 PM	M. Tech (Electronics and Telecommunication Engineering)	MTETE125C	Optical Fiber Communication
14	31-07-2024	2.00 PM to 5.00 PM	M.Tech Electronics and Communication (VLSI Design)	MTVLSIE125B	System Design with Embedded Linux
15	31-07-2024	2.00 PM to 5.00 PM	M. Tech (VLSI & Embedded Systems)	MTVEE125B	Network Security and Cryptography
16	31-07-2024	2.00 PM to 5.00 PM	M. Tech (VLSI & Embedded Systems)	MTVEE125C	CMOS Digital Integrated Circuit Design
17	31-07-2024	2.00 PM to 5.00 PM	M. Tech (VLSI & Embedded Systems)	MTVEE125D	Embedded C

**Important Note:** 1.The candidates appearing for the examination should report 20 minutes before the start of examination.  
2. Mobile phones and other electronic gadgets are prohibited in the examination hall.

Copy to:

- Hon 'ble Vice Chancellor (For Information)
- The Registrar (For Information)
- Principal/Director of all the affiliated institutes.
- OSD,Regional centers/Sub Centers
- Students Notice Board



  
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