



डॉ.बाबासाहेब आंबेडकर तंत्रशास्त्र विद्यापीठ, लोणेरे
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परीक्षा विभाग

Dr. Narendra S. Jadhav

Controller of Examination(I/c)

डॉ. नरेंद्र सु. जाधव

परीक्षा नियंत्रक (प्र.)

Ref no: DBATU/Reg._Suppli.Exam/B.Tech/TT/2023/1388

Date: 14/12/2023

B.Tech Time Table Regular & Supplementary Winter - 2023
Electronics Engineering and Allied (11376/11372/11370/11844/11900/11378)

Sr. No.	Date	Time	Course Name	Subject Code	Subject Name
1	18-12-2023	10:00 am To 1:00 pm	Electronics Engineering / Electronics and Telecommunication Engineering/Electronics and Communication Engineering/VLSI Design & Technology	BTBS301	Engineering Mathematics - III
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTES301	Engineering Mathematics-III
2	20-12-2023	10:00 am To 1:00 pm	Electronics Engineering/ Electronics and Telecommunication Engineering / Electronics and Communication Engineering	BTEXC302/BTETC302	Electronic Devices & Circuits
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECPC302	Electronics Devices & Circuits
			VLSI Design & Technology	BTVTC302	Electronic Device & Circuits
			Electronics Engineering/ Electronics and Telecommunication Engineering	BTEXC302_Y18	Analog Circuits
3	22-12-2023	10:00 am To 1:00 pm	Electronics Engineering/ Electronics and Telecommunication Engineering / Electronics and Communication Engineering	BTEXC303/BTETC303	Digital Electronics
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECPC303	Programming, Data Structure and Algorithm using C
			VLSI Design & Technology	BTVTC303	Digital System Design
			Electronics Engineering/ Electronics and Telecommunication Engineering	BTEXC303_Y18	Electronic Devices & Circuits
4	27-12-2023	10:00 am To 1:00 pm	Electronics Engineering	BTEXC304	Network Theory
			Electronics and Telecommunication Engineering / Electronics and Communication Engineering	BTES304	Electrical Machines and Instruments
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTESC304_EC	Computer Architecture & Operating System
			VLSI Design & Technology	BTVTC304	Signals and Systems
5	29-12-2023	10:00 am To 1:00 pm	Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTESC305_EC	Digital Electronics and Microprocessor
			VLSI Design & Technology	BTVTC305	Network Theory
			Electronics Engineering/ Electronics and Telecommunication Engineering	BTEXC305_Y18	Digital Logic Design

- Important Note:**
- The candidates appearing for the examination should report 20 minutes before the start of examination.
 - Mobile phones and other electronic gazets are prohibited in the examination hall.
 - Change if any, in the time table shall be communicated on the University web site.

Copy to:

- Hon'ble Vice Chancellor (For Information)
- The Registrar (For Information)
- Principal/Director of all the affiliated institutes.
- OSD, Regional centers/Sub Centers
- Students Notice Board
- University Website- www.dbatu.ac.in



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