Dr. Narendra S. Jadhav
Controller of Examination(I/c)
Ref no: DBATU/Reg._Suupli.Exam/B.Tech/TT/2023/1408

डॉ. नरेंद्र सु. जाधव
परीक्षा नियंत्रक (प्र.)
Date: 18/12/2023

B.Tech Time Table Regular & Supplementary Winter - 2023 (Rescheduled)
Electronics Engineering and Allied (11376/11372/11370/11844/11900)

Sr. No.	Date	Time	Course Name	Subject Code	Subject Name
1	01-01-2024	2:00 PM To 5:00 PM	Electronics Engineering	BTEXC501	Analog Circuits
			Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTETC501	Electromagnetic Field Theory
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECPC501	Computer Networks and Cloud Computing
			Electronics and Telecommunication Engineering	BTEXC501_Y18	Electromagnetic Field Theory
2	03-01-2024	2:00 PM To 5:00 PM	Electronics Engineering/ Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTEXC502/BTETC502	Digital Signal Processing
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECPC502	Digital Signal & Image Processing
			Electronics and Telecommunication Engineering	BTEXC502_Y18	Control System Engineering
3	05-01-2024	2:00 PM To 5:00 PM	Electronics Engineering	BTEXC503	Microelectronics
			Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTETC503	Analog Communication
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECPE503A	Sensors and Robotics Technology
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECPE503B	Data Warehouse & Data Mining
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECPE503C	Wireless & Mobile Computing
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECPE503D	Software Engineering
			Electronics and Telecommunication Engineering	BTETC503_Y18	Computer Architecture
4	08-01-2024	2:00 PM To 5:00 PM	Electronics Engineering	BTEXPE504A	Electromagnetic Field Theory
			Electronics Engineering	BTEXPE504B	VLSI Design & Technology
			Electronics Engineering	BTEXPE504C	Electronics in Smart City
			Electronics Engineering	BTEXPE504D	Electronics Measurements and Instruments
			Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTETPE504A	Analog Circuits
			Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTETPE504B	Embedded System Design
			Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTETPE504C	Digital System Design
			Electronics Engineering/ Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTEXPE504F/BTETPE504D	Automotive Electronics
			Electronics Engineering/ Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTEXPE504E/BTETPE504E	Mixed Signal Design
			Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTETPE504F	Power Electronics
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECOE504A	Microelectronics Devices and Circuits
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECOE504B	Analog & Digital Communication
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECOE504C	Programming in JAVA
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECOE504D	Electrical Machines and Instrumentation
			Electronics and Telecommunication Engineering	BTEXC504_Y18	Digital Signal Processing

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			Electronics Engineering	BTEXOE505A	Digital System Design
5	10-01-2024	2:00 PM To 5:00 PM	Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTETOE505A	Control System Engineering
			Electronics Engineering/ Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTEXOE505B/BTETOE505B	Artificial Intelligence and Machine learning
			Electronics Engineering/ Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTEXOE505C/BTETOE505C	Optimization Techniques
			Electronics Engineering	BTEXOE505D	Project Management and Operation Research
			Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTETOE505D	Project Management and Operation Research
			Electronics Engineering/ Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTEXOE505E/BTETOE505E	Augmented, Virtual and Mixed Reality
			Electronics & Telecommunication Engineering/Electronics &Communication Engineering	BTETOE505F	Open-Source Technologies
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECHM505A	Economics and Management
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTECHM505B	Business Communication
			Electronics and Telecommunication Engineering	BTEXC505_Y18	Microcontroller and its Application
6	12-01-2024	2:00 PM To 5:00 PM	Electronics and Telecommunication Engineering	BTEXPE506C_Y18	Data Structure & Algorithms Using Java Programming

Important Note: 1.The candidates appearing for the examination should report 20 minutes before the start of examination.

2. Mobile phones and other electronic gadgets are prohibited in the examination hall.

3.Change if any, in the time table shall be communicated on the University web site.

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1. Hon 'ble Vice Chancellor (For Information)
2.The Registrar (For Information)
3.Principal/Director of all the affiliated institutes.
4.OSD,Regional centers/Sub Centers
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6.University Website- www.dbatu.ac.in

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