



डॉ.बाबासाहेब आंबेडकर तंत्रशास्त्र विद्यापीठ, लोणेरे
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परीक्षा विभाग

Dr. Narendra S. Jadhav
Controller of Examination(I/c)

डॉ. नरेंद्र सु. जाधव
परीक्षा नियंत्रक (प्र.)
Date:- 30/12/2023

Ref no: DBATU/Reg. Suppl. Exam/B.Tech/TT/2023/1475

Reschedule B.Tech Time Table Regular & Supplementary Winter - 2023
Electronics Engineering and Allied (11376/11372/11370/11844/11900/11378)

Sr. No.	Date	Time	Course Name	Subject Code	Subject Name
1	02-01-2024	10:00 am To 1:00 pm	Electronics Engineering / Electronics and Telecommunication Engineering/Electronics and Communication Engineering/VLSI Design & Technology	BTBS301	Engineering Mathematics - III
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTES301	Engineering Mathematics-III
2	04-01-2024	10:00 am To 1:00 pm	Electronics Engineering/ Electronics and Telecommunication Engineering / Electronics and Communication Engineering	BTEXC302/BTETC302	Electronic Devices & Circuits
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTEPC302	Electronics Devices & Circuits
			VLSI Design & Technology	BTVTC302	Electronic Device & Circuits
			Electronics Engineering/ Electronics and Telecommunication Engineering	BTEXC302_Y18	Analog Circuits
3	06-01-2024	10:00 am To 1:00 pm	Electronics Engineering/ Electronics and Telecommunication Engineering / Electronics and Communication Engineering	BTEXC303/BTETC303	Digital Electronics
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTEPC303	Programming, Data Structure and Algorithm using C
			VLSI Design & Technology	BTVTC303	Digital System Design
			Electronics Engineering/ Electronics and Telecommunication Engineering	BTEXC303_Y18	Electronic Devices & Circuits
4	09-01-2024	10:00 am To 1:00 pm	Electronics Engineering	BTEXC304	Network Theory
			Electronics and Telecommunication Engineering / Electronics and Communication Engineering	BTES304	Electrical Machines and Instruments
			Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTESC304_EC	Computer Architecture & Operating System
			VLSI Design & Technology	BTVTC304	Signals and Systems
			Electronics and Telecommunication Engineering	BTEXC304_Y18	Network Analysis
5	11-01-2024	10:00 am To 1:00 pm	Electronics and Computer Engineering / Electronics and Computer Science Engineering	BTESC305_EC	Digital Electronics and Microprocessor
			VLSI Design & Technology	BTVTC305	Network Theory
			Electronics Engineering/ Electronics and Telecommunication Engineering	BTEXC305_Y18	Digital Logic Design

Important Note: 1.The candidates appearing for the examination should report 20 minutes before the start of examination.
2. Mobile phones and other electronic gadgets are prohibited in the examination hall.
3.Change if any, in the time table shall be communicated on the University web site.

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1. Hon'ble Vice Chancellor (For Information)
2.The Registrar (For Information)
3.Principal/Director of all the affiliated institutes.
4.OSD,Regional centers/Sub Centers
5.Students Notice Board
6.University Website- www.dbatu.ac.in



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