CURRICULUM VITAE
Of
PRASHANT P. MAHAJAN

Name	Prashant Prabhakar Mahajan
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Profile

Teaching experience of 6 years in UG engineering colleges of University of Mumbai. Commitment to deliver quality education with focus on outcome base education.

Educational Qualification:

Qualification	High School/College/ University	Year of Pass	Division	%
10th	R.B. Shirke High School Ratnagiri.	2002	1st	**74**
HSC	Abhyankar Kulkarni Junior College Ratnagiri.	2004	1st	**74**
B.E. Electronics	Finolex Academy of Management and Technology.	2008	1st	**62**
Post-graduation diploma in VLSI	Sandeepani School of VLSI design, Bangalore.	2009	1st	**72**
M.TECH (E&TC)	Mukesh Patel School of Technology Management and Engineering (MPSTME). Vile-Parle Mumbai	2017	1st	**74**

Computer Proficiency: -

Operating systems	Windows 7, 8 and 10, Linux
Application software	P-spice, Micro wind, LTspice, Cadence, ADS, Icarus Verilog, Xilinx ISE, MATLAB, Multisim
Mailing systems	Outlook, Gmail
Office software	Word, Excel and PowerPoint

Professional Teaching Experience

Finolex Academy of Management & Technology (16.07.2010 to 31.05.2011)

Worked as a Lecturer in Electronics department.

Gharda Institute of technology (**23.07.2012 to 10.11.12**)

Worked as an Assistant Professor in Electronics & Telecommunication department.

Maharshi Parshuram College of Engineering (26.11.2012 to 23.10.2019)

Worked as an Assistant Professor for Electronics and Telecommunication Engineering department.

English proficiency

IELTS score 7.0 out of 10 in 2011

Languages

Name	Read	Write	Speak
English	Yes	Yes	Yes
Marathi	Yes	Yes	Yes
Hindi	Yes	Yes	Yes

Subjects taught in professional teaching experience: -

Subjects taught	Branch	Semester	Year	Number of times
Basics of electrical and electronics Engineering	First year	I	First	2
Circuits and Transmission Lines	EXTC	III	Second	2
Analog Electronics	EXTC	IV	Second	1
Integrated Circuits	EXTC	V	Third	2
Discrete time signal processing	EXTC	VI	Third	1
Digital signal & image processing	Computer	VII	Fourth	1
Industrial Electronics	B.Tech (integrated) Mechanical	V	Third (NMIMS)	1
Telecommunication management network (TMN)	EXTC	VIII	Fourth	1
Internet communication Engineering (ICE)	EXTC	VII	Fourth	1
Mechatronics	Mechanical	VI	Third	1
VLSI design	EXTC	VI	Third	2
OOPJ	EXTC	III	Second	1

Research

Presented paper titled "Analyzing two-stage CMOS LNA for wireless interconnect" at I2CT 2019: IEEE 5th Conference for Convergence in Technology 2019, Pune India on 30th March 2019.

Personal training & extracurricular activity

1. Completed post-graduation diploma in VLSI design at Sandeepani School of VLSI design, Bangalore. Verilog HDL was used to program Spartan and Virtex kits.

2. Worked as Assistant Coordinator for event 'iMACbotz' held in GIT, Lavel in Sept.2012.

3. Attended workshop on 'AAKASH TABLET' by IIT Bombay at VPMMPCOE, Velneshwar, Ratnagiri.

4. Attended 2-days FDP on 'Laboratory experiment with cadence tools for Analog VLSI design' at SPIT Andheri, Mumbai.

5. Attended AICTE sponsored one-week workshop on 'Mechatronics and Industrial Automation' at COEP Pune 26th to 30 March 2018.

6. Attended AICTE sponsored one-week workshop on "Hands-on approach to semiconductor device physics" at IIT Bombay, on 6th to 11th May 2019.

I hereby declare that all the information furnished above is true and genuine to the best of my knowledge and belief.

Place : Ratnagiri.

Date : 02/01/2021

Prashant Prabhakar Mahajan