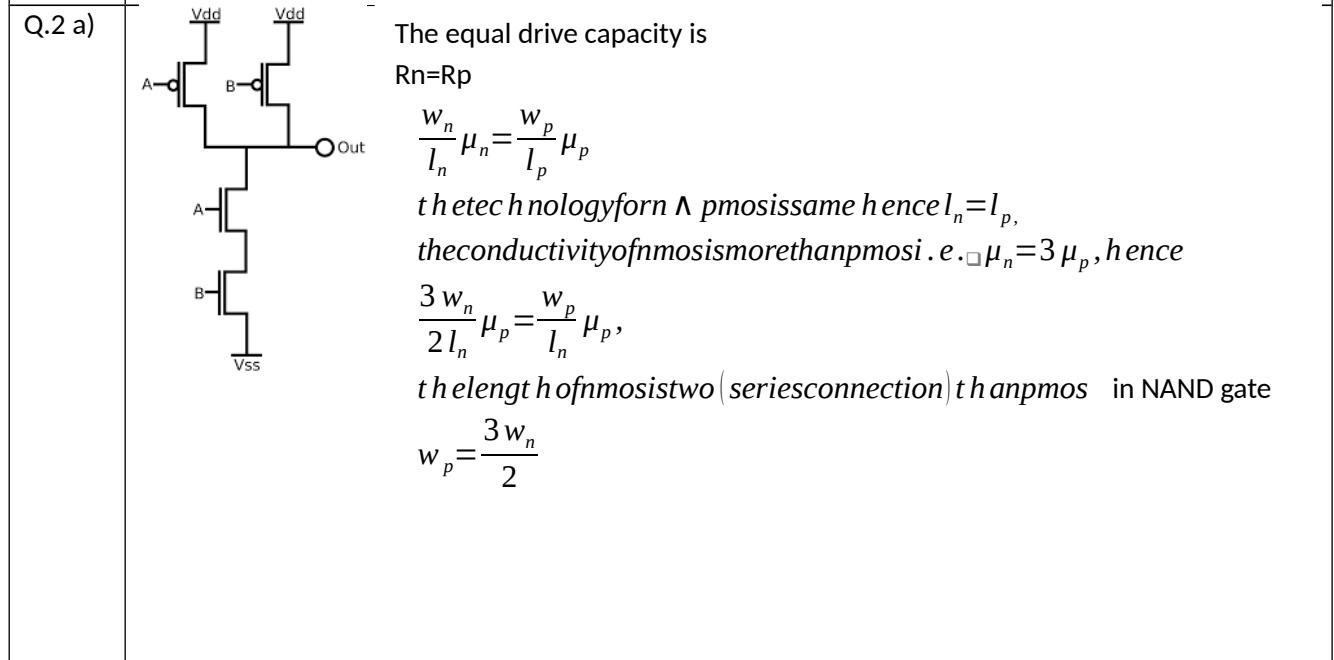
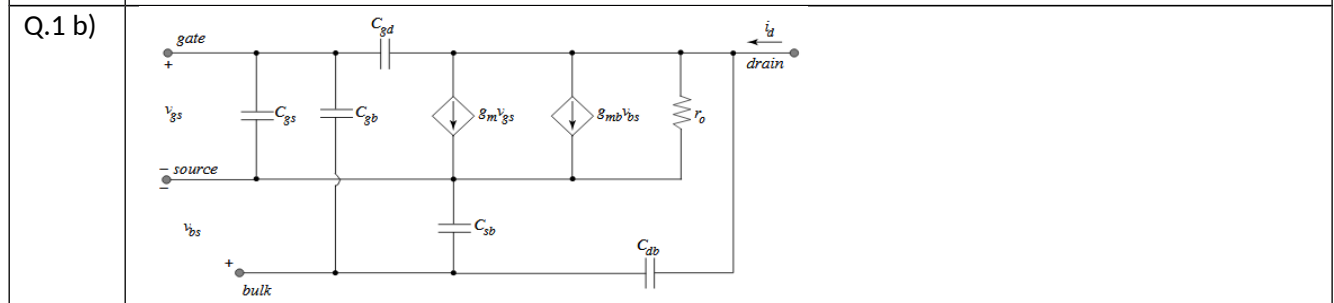
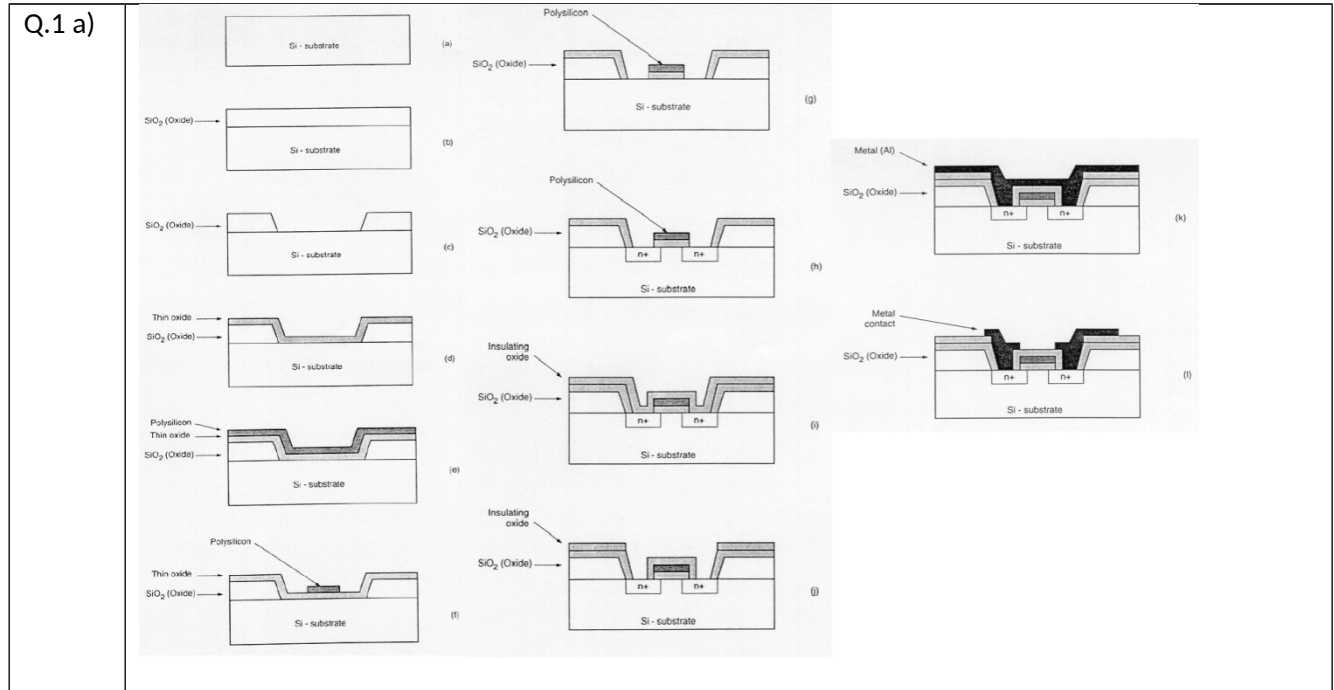


# Model Answer:

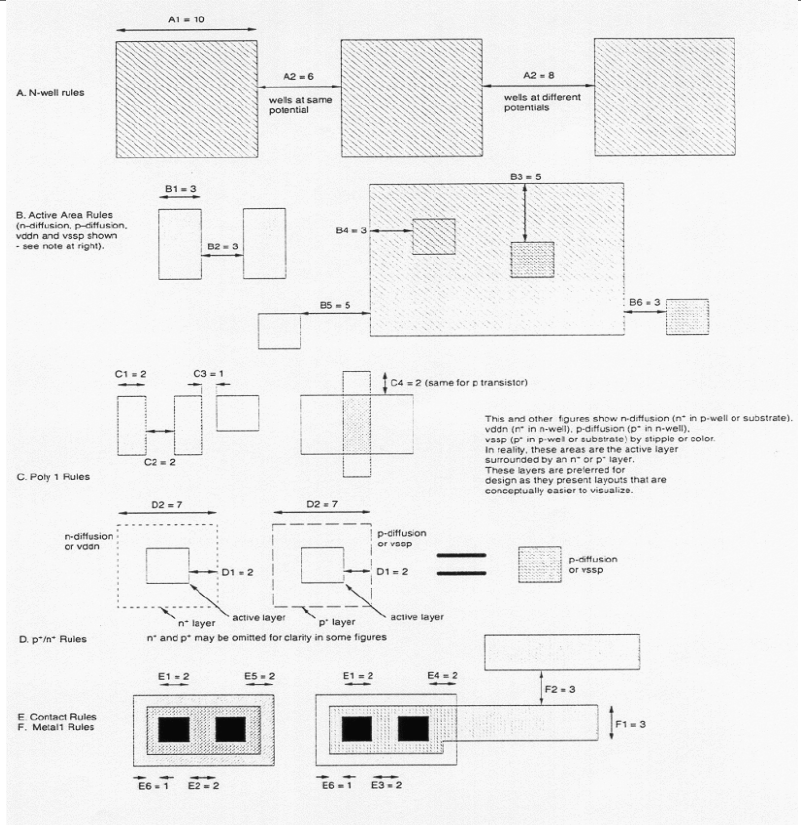
Branch: M. Tech.

Sem.:- I

Subject with Subject Code:-CMOS Analog Circuit Design ( )



b)



c)

The resistance and capacitance are the two passive components of MOS device. The resistance is due to the polysilicon or metal layers. The capacitance is formed due to the structure of MOS. The capacitance between gate and other layers (source, drain, substrate).

d)

