Course Structure and Syllabus

For

M. Tech. (Electronics & Communication Engineering)

Two Year (Four Semester) Course

(w.e.f. July 2017)
M.Tech. (Electronics & Communication Engineering)

Objectives

I. To serve the society and nation, by providing high quality engineering educational programs to the students, engaging in research and innovations that will enhance the skill and knowledge and assisting the economic development of the region, state, and nation through technology transfer.

II. To equip the postgraduate students with the state of the art education through research and collaborative work experience/culture to enable successful, innovative, and life-long careers in Electronics and Telecommunication.

III. To encourage the post-graduates students, to acquire the academic excellence and skills necessary to work as Electronics and Telecommunication professional in a modern, ever-evolving world.

IV. To provide the broad understanding of social, ethical and professional issues of contemporary engineering practice and related technologies, as well as professional, ethical, and societal responsibilities.

V. To inculcate the skills for perusing inventive concept to provide solutions to industrial, social or nation problem.

Outcomes

I. Students of this program will have ability to apply knowledge of mathematics, sciences and engineering to Electronics and Telecommunication problems.

II. Postgraduate students will gain an ability to design and conduct experiments, as well as to analyze and interpret data/results.

III. Learners of this program will built an ability to design and develop a system, components, devices, or process to meet desired needs.

IV. Masters students of this program will have an ability to work on multi-disciplinary teams and also as an individual for solving issues related to Electronics and Telecommunication.

V. Learners of this program will have an ability to identify, formulate, and solve Engineering problems by applying mathematical foundations, algorithmic principles, and Electronics and Telecommunication theory in the modeling and design of electronics systems in a way that demonstrates comprehension of the tradeoffs involved in design choices.

VI. Postgraduate students will have an ability to communicate effectively orally and in writing and also understanding of professional and ethical responsibility.

VII. Postgraduate students will have an ability to use the techniques, skills, and modern engineering EDA tools necessary for Electronics and Telecommunication practices.

VIII. Learners of this program will have an ability to evaluate Electronics and Telecommunication Engineering problems with cost effectiveness, features, and user friendliness to cater needs for innovative product development.

IX. Postgraduate students will have an ability to solve contemporary social and industrial problems by engaging in life-long learning.
Dr. Babasaheb Ambedkar Technological University
Teaching and Examination Scheme for
M.Tech. (Electronics & Communication) w.e.f. July 2017

<table>
<thead>
<tr>
<th>Sr. No.</th>
<th>Course Code</th>
<th>Name of the course</th>
<th>Hours/Week</th>
<th>Credit</th>
<th>Examination scheme</th>
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**First Semester**

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<tr>
<td>01</td>
<td>MTECC101</td>
<td>Advanced Communication Engineering</td>
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<td>Wireless Communication</td>
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<td>03</td>
<td>MTECC103</td>
<td>Signal Processing Algorithms &amp; Applications</td>
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<td>MTECE125</td>
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<td>03</td>
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<td>06</td>
<td>MTECC1106</td>
<td>Communication Skills</td>
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<td>07</td>
<td>MTECL107</td>
<td>PG Lab-I*</td>
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**Total for Semester I**

17 03 03 22 300 100 150 50 600

**Second Semester**

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<td>01</td>
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<td>Image and Video Processing</td>
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<td>Information Theory and Coding</td>
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**Total for Semester II**

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**Third Semester**

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**Total for Semester III**

-- -- -- 12 -- -- 100 100 200

**Fourth Semester**

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**Total for Semester IV**

-- -- -- 20 -- -- 100 100 200

**GRAND TOTAL**

1700

* PG Lab-I – Practical shall be based on courses of first semester.
# Student has to choose this course either from NPTEL/MOOC pool and submission of course completion certificate is mandatory.
Elective-I
A. Artificial Neural Networks and Applications
B. Electronics Interference and Compatibility
C. Analog and Mixed Signal Processing
D. Low Power VLSI Circuit & System
E. Smart Antenna

Elective-II
A. Remote Sensing Systems
B. Advanced Radar System
C. Optical Fiber Communication
D. Statistical Signal Processing
E. Microelectronics

Elective-III
A. Multirate Digital Signal Processing
B. Embedded System Design
C. Wireless Sensor Network Design
D. Microwave Design and Measurement
E. Estimation and Detection Theory

Elective-IV
A. Advanced Biomedical Signal Processing
B. Reconfigurable Computing
C. Digital VLSI Design
D. Networks and Systems Security
E. Multicarrier Communication

Elective-V (Open)
A. Internet of Things
B. Linear Algebra
C. Multimedia Communication
D. Research Methodology
E. Wavelet Transforms and its Applications
ADVANCED COMMUNICATION ENGINEERING

Weekly Teaching Hours

| TH: 03 | Tut: 01 |

Scheme of Marking

| TH: 60 | Tests: 20 | IA: 20 | Total: 100 |

Course Objectives:

| A       | To provide basic knowledge in the field of Advanced Communication Engineering |
| B       | To develop greater awareness and sensitivity to some important considerations of advanced communication |

Course Outcomes:

| CO1     | Learner will learn how to design and represent band pass signal |
| CO2     | Learner will be able to analyze digital modulation techniques and their reception |
| CO3     | Learner will be able to demonstrate basic principles of data communication and different digital data transmission formats |
| CO4     | Learner will learn how to derive BER and PEP expression |
| CO5     | Learner will be know the different transmission principles |
| CO6     | Learner will know different types of broadband networks and different networking aspects. |

Unit I

Spread Spectrum Communication: Direct sequence and frequency hopped spread spectrum, spreading sequences and their correlation functions, Acquisition and tracking of spread spectrum signals Code, Division Multiple Access (CDMA): DS-CDMA on AWGN channels, DS-CDMA on frequency selective fading channels, Performance analysis of cellular DS-CDMA, Capacity estimation, Power control effect of imperfect power control on DS-CDMA performance, Soft Hand offs, Spreading/coding tradeoffs, multi carrier CDMA, IS95A CDMA systems, 3rd Generation CDMA systems,

UNIT II


UNIT III

Data Communication Networks: Basic principles of data communication – synchronous and asynchronous transmission – digital data transmission formats NRZ, RZ, AMI, ASI & Manchester coding, Error correcting codes, Hamming codes, Orthogonal codes, Switching – Circuit switching, Message switching, Packet switching, Standard communication interface multipliers and concentrators,

UNIT IV

UNIT V

UNIT VI

Text Books/Reference:

5. Andrew S Tanenbaum, Computer Networks, Prentice Hall of India.
WIRELESS COMMUNICATION

Weekly Teaching Hours
TH : 03    Tut:  01

Scheme of Marking
TH :60    Tests : 20    IA: 20    Total : 100

Course Objectives:

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<tbody>
<tr>
<td>A</td>
<td>To provide in depth understanding for fundamental concepts of modern mobile and wireless communication system</td>
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<tr>
<td>B</td>
<td>To develop understanding on the challenges and opportunities in the wireless medium in designing current and future wireless communication systems and networks.</td>
</tr>
</tbody>
</table>

Course Outcomes:

| CO1 | Learner will be able to analyze different services and their requirements. |
| CO2 | Learner will be able to analyze and design receiver and transmitter diversity techniques |
| CO3 | Learner will be able to determine the appropriate transceiver design of multi-antenna systems and evaluate the data rate performance. |
| CO4 | Learner will be able to Design wireless communication systems with key 3G (e.g.,CDMA) and 4G (OFDM) technologies |
| CO5 | Learner will be able to Describe and differentiate four generations of wireless standard for cellular networks. |

UNIT I

Services And Technical Challenges:
Types of Services, Requirements for the services, Multipath propagation, Spectrum Limitations, Noise and Interference limited systems, Principles of Cellular networks, Multiple Access Schemes.

UNIT II

Wireless Propagation Channels
Propagation Mechanisms (Qualitative treatment), Propagation effects with mobile radio, Channel Classification, Link calculations, Narrowband and Wideband models.

UNIT III

Wireless Transceivers
Structure of a wireless communication link, Modulation and demodulation – Quadrature Phase Shift Keying, p/4-Differential Quadrature Phase Shift Keying, Offset-Quadrature Phase Shift Keying, Binary Frequency Shift Keying, Minimum Shift Keying, Gaussian Minimum Shift Keying, Power spectrum and Error performance in fading channels.
UNIT IV
Signal Processing In Wireless Systems
Principle of Diversity, Macrodiversity, Microdiversity, Signal Combining Techniques, Transmit diversity, Equalisers- Linear and Decision Feedback equalisers, Review of Channel coding and Speech coding techniques.

UNIT V
Advanced Transceiver Schemes
Spread Spectrum Systems- Cellular Code Division Multiple Access Systems- Principle, Power control, Effects of multipath propagation on Code Division Multiple Access, Orthogonal Frequency Division Multiplexing – Principle, Cyclic Prefix, Transceiver implementation, Second Generation(GSM, IS–95) and Third Generation Wireless Networks and Standards.

UNIT VI
3G and 4G Wireless Standards
GSM, GPRS, WCDMA, LTE, WiMAX.

Text Books/Reference:
SIGNAL PROCESSING ALGORITHMS AND APPLICATIONS

Weekly Teaching Hours  TH : 03  Tut:  01
Scheme of Marking      TH :60  Tests : 20  IA: 20  Total : 100

Course Objectives:

A  To install research skills and bring in optimal solutions and novel products to signal processing and allied application areas using modern technology and tools that are technically sound, economically feasible and socially acceptable.

B  To enable the graduates to engage in signal processing and its broad range of applications to understand the challenges of the rapidly changing environment and adapt their skills through reflective and continuous learning.

C  To provide graduates strong mathematical skills and in depth knowledge in signal theory to analyze and solve complex problems in the domain of signal processing.

Course Outcomes:

CO1  Learner will be able to analyze the time and frequency response of discrete time system.

CO2  Learner will be able to design digital filters for various applications.

CO3  Learner will be able to design FIR and IIR filters for various applications.

CO4  Learner will be able to understand the fundamentals of multi rate signal processing and its application.

CO5  Learner will be able to understand signal representation in terms of dimension, orthogonality etc.

CO6  Learner will be able to analyze least square method for power spectrum estimation.

UNIT I

Introduction

Review of discrete time signals and systems, Different transforms, Filtering, Use of DFT in linear filtering, Filtering of long data sequences, Spectrum, Algorithm for convolution and DFT.
UNIT II
LTI DT System in Transform Domain and Digital Filter Structures
Simple Digital Filters, All Pass, Linear Phase and Minimum & Maximum phase and Complementary transfer Functions. Basic FIR and IIR Digital Filter Structures, Linear Phase Structure IIR, FIR and All pass Lattice Structure.

UNIT III
Design of Digital Filters

UNIT IV
Multirate Signal Processing
Filter banks, Interpolators, Decimators, Polyphase decomposition, Analysis and synthesis, Orthogonal and orthonormal filter banks.

UNIT V
Signal Representation
Representation of deterministic signals, orthogonal representation of signals, Dimensionality of signal spaces, Construction of orthogonal basis functions, Time-bandwidth relationship, RMS duration and bandwidth, Uncertainty relations, Multiresolution Analysis and Wavelet Transform.

UNIT VI
Linear Prediction and Optimum Filter Design
Least square methods for system modeling, Adaptive filters, Power spectrum estimation.

Text Books/Reference:
2. A V Oppenheim, Schafer, Discrete Time Signal Processing; PHI.
4. P P Vaidyanathan, Multirate systems and Filter Banks; Prentice Hall Eaglewood.
6. S Hykin, Adaptive Filter Theory; PHI.
ELECTIVE-I
ARTIFICIAL NEURAL NETWORKS AND APPLICATIONS

Weekly Teaching Hours
TH : 03  Tut:  --

Scheme of Marking
TH: 60  Tests: 20  IA: 20  Total: 100

Course Objectives:

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<tbody>
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<td>A</td>
<td>To provide in-depth understanding of fundamental theory and concepts of computational intelligence methods</td>
</tr>
<tr>
<td>B</td>
<td>To understand the fundamental theory and concepts of neural networks, neuro-modeling, several neural network paradigms and its applications.</td>
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Course Outcomes:

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<tbody>
<tr>
<td>CO1</td>
<td>Learner will be able to articulate analogy of human neural network for understanding of artificial learning algorithms.</td>
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<tr>
<td>CO2</td>
<td>Learner will be able to analyze radial basis function network.</td>
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<tr>
<td>CO3</td>
<td>Learner will be able to analyze neural network architecture &amp; basic learning algorithms.</td>
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<tr>
<td>CO4</td>
<td>Learner will be able to understand mathematical modeling of neurons, neural networks.</td>
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<tr>
<td>CO5</td>
<td>Learner will be able to analyze training, verification and validation of neural network models.</td>
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<tr>
<td>CO6</td>
<td>Learner will be able to design Engineering applications that can learn using neural networks.</td>
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</table>

UNIT I

UNIT II
Functions, Mathematical Preliminaries, Artificial Neurons, Neural Networks and Architectures Pattern analysis tasks: Classification, Clustering, mathematical models of neurons, Structures of neural networks, learning principles.

UNIT III

UNIT IV
Auto-associative neural networks, Pattern storage and retrieval, Hopfield model, recurrent neural networks, Bayesian neural networks,
UNIT V
Radial basis function networks: Regularization theory, RBF networks for function approximation, RBF networks for pattern classification

UNIT VI
Self-organizing maps: Pattern clustering, Topological mapping, Kohonen’s self-organizing map Introduction to cellular neural network, Fuzzy neural networks, and Pulsed neuron models recent trends in Neural Networks

Text Books/Reference:
**ELECTIVE-I**

**ELECTROMAGNETIC INTERFERENCE AND COMPATIBILITY**

Weekly Teaching Hours  
TH : 03 Tut:  --  

Scheme of Marking  
TH : 60 Tests : 20 IA: 20 Total : 100

**Course Objectives:**

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<tbody>
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<td>A</td>
<td>To familiarize with the fundamentals that are essential for electronics industry in the field of EMI / EMC</td>
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<td>B</td>
<td>To understand EMI sources and its measurements.</td>
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<tr>
<td>C</td>
<td>To understand the various techniques for electromagnetic compatibility.</td>
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</table>

**Course Outcomes:**

<table>
<thead>
<tr>
<th>CO</th>
<th>Learner will acquire knowledge of EMI / EMC sources and their standards</th>
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<tbody>
<tr>
<td>CO2</td>
<td>Learner will be able to measure different parameters of interference in EM</td>
</tr>
<tr>
<td>CO3</td>
<td>Learner will be able to reduce the interference within EM devices</td>
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<tr>
<td>CO4</td>
<td>Learner will be able to illustrate the physical and statistical model of EM devices</td>
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<tr>
<td>CO5</td>
<td>Learner will be able to analyze the EM devices in terms of Computer Based Modeling and Simulation</td>
</tr>
<tr>
<td>CO6</td>
<td>Learner will be able to design electronic systems that function without errors or problems related to electromagnetic compatibility.</td>
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**UNIT I**

**Introduction to EMI / EMC:**
EMI / EMC Standards, Introduction to E, H, Near and far field radiators, Receptors and antennas, Different types of EMI sources and possible remedies.

**UNIT II**

**Measurement techniques in EMI:**
Open area test sites, Radiated interference measurements, Conducted interference measurements, Interference immunity.

**UNIT III**

**EMI reduction techniques:**
Grounding, Shielding, Bonding, and EMI filters.

**UNIT IV**

**Probabilistic and Statistical Physical Model:**

**UNIT V**

**Computer Based Modeling and Simulation:**
Computer Based Modeling and Simulation of EMI Models and Signal Integrity.

Text Books /Reference:
1. V. Prasad Kodali, Engineering Electromagnetic Compatibility, Principles and Measurement Technologies; IEEE Press
ELECTIVE-I
ANALOG AND MIXED SIGNAL PROCESSING

Weekly Teaching Hours
TH : 03 Tut: --

Scheme of Marking
TH : 60 Tests : 20 IA: 20 Total : 100

Course Objectives:

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<tr>
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<th>To provide in-depth understanding of the fundamental concepts of analog signal processing.</th>
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<tbody>
<tr>
<td>B</td>
<td>To provide in-depth understanding of data conversion, PLL design, filter design.</td>
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</table>

Course Outcomes:

<table>
<thead>
<tr>
<th>CO1</th>
<th>Learner will be able to distinguish between fundamental concepts of analog and discrete time signal processing.</th>
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<tbody>
<tr>
<td>CO2</td>
<td>Learner will be able to design switched capacitor filters.</td>
</tr>
<tr>
<td>CO3</td>
<td>Learner will be able to demonstrate basics of analog to digital data conversion.</td>
</tr>
<tr>
<td>CO4</td>
<td>Learner will be able to design analog and digital PLLs.</td>
</tr>
<tr>
<td>CO5</td>
<td>Learner will be able to understand fundamentals of green data converters.</td>
</tr>
</tbody>
</table>

UNIT I
Switched Capacitor filters: Introduction to Analog and Discrete Time signal processing, sampling theory, Nyquist and over sampling rates, Analog filters, analog amplifiers, lock in amplifiers.

UNIT II
Analog integrated and discrete time switched capacitor filters, non-idealities in switched capacitor filters, architectures for switched capacitor filters and their applications and design. Switched capacitor amplifiers.

UNIT III
Data converters: Basics of data converters, Types of data converters, types of ADCs, Successive approximation, dual slope, Flash type, pipelined ADCs, hybrid ADCs, high resolution ADCs, parallel path ADCs like time-interleaved and multi-channel converters.

UNIT IV
Types of DACs and their architectures, binary weighted DACs. Performance metrics of data converters, SNR, SFDR, SNDR.

UNIT V
Background and foreground techniques to improve performance of data converters, Green data converters (low power design).
UNIT VI

Frequency synthesizers and synchronization: Analog PLLs, Digital PLLs design and architectures, Delay locked loops design and architectures. Direct Digital Synthesis.

Text/Reference Books

1. R. Jacob Baker, CMOS mixed-signal circuit design, Wiley India, IEEE press, reprint 2008
4. Walt Kester, Mixed Signal and DSP Design techniques, Engineering Analog Devices Inc, Engineering Analog Devices Inc, , Publisher Newnes.
5. Bar-Giora Goldberg, Digital Frequency Synthesis Demystified, Published by Elsevier.
ELECTIVE-I
LOW POWER VLSI CIRCUIT & SYSTEM

Weekly Teaching Hours
TH : 03 Tut: --

Scheme of Marking
TH :60 Tests : 20 IA: 20 Total : 100

Course Objectives:

| A | To provide in-depth understanding of the fundamental concepts of VLSI circuit and system. |
| B | To provide various strategies and methodologies for designing low power circuit and Systems. |

Course Outcomes:

| CO1 | Learner will be able to distinguish between different logic families. |
| CO2 | Learner will be able to understand different sources of power dissipation |
| CO3 | Learner will be able to understand the supply voltage scaling approaches |
| CO4 | Learner will be able to understand capacitance minimization approaches |
| CO5 | Learner will be able to differentiate between leakage power minimization approaches. |

UNIT I
BASICS OF MOS CIRCUITS:
MOS Transistor structure and device modelling, MOS Inverters, MOS Combinational Circuits - Different Logic Families

UNIT II
SOURCES OF POWER DISSIPATION:

UNIT III
SUPPLY VOLTAGE SCALING APPROACHES:
Device feature size scaling, Multi-Vdd Circuits, Architectural level approaches: Parallelism, Pipelining, Voltage scaling using high-level transformations, Dynamic voltage scaling, Power Management

UNIT IV
CAPACITANCE MINIMIZATION APPROACHES:
Hardware Software Tradeoff, Bus Encoding, Two’s complement Vs Sign Magnitude, Architectural optimization, Clock Gating, Logic styles

UNIT V
LEAKAGE POWER MINIMIZATION APPROACHES:
Variable-threshold-voltage CMOS (VTCMOS) approach, Multi-threshold-voltage CMOS (MTCMOS) approach, Power gating, Transistor stacking, Dual-Vt assignment approach (DTCMOS)
UNIT VI
SPECIAL TOPICS:
Adiabatic Switching Circuits, Battery-aware Synthesis, Variation tolerant design CAD tools for low power synthesis

Text Books /Reference:

**ELECTIVE-I**

**SMART ANTENNA**

Weekly Teaching Hours  
TH : 03  
Tut: --

Scheme of Marking  
TH :60  
Tests : 20  
IA: 20  
Total : 100

<table>
<thead>
<tr>
<th>Course Objectives:</th>
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<tbody>
<tr>
<td>A</td>
</tr>
<tr>
<td>To provide in-depth understanding of modern antenna concepts, and practical antenna design for various applications</td>
</tr>
<tr>
<td>B</td>
</tr>
<tr>
<td>To provide in-depth understanding of of smart antenna concept with a view that the student can further explore the topic for research purpose.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Course Outcomes:</th>
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</thead>
<tbody>
<tr>
<td>CO1</td>
</tr>
<tr>
<td>Learner will be able to compare the performances of digital radio receivers and software radios.</td>
</tr>
<tr>
<td>CO2</td>
</tr>
<tr>
<td>Learner will be able to understand the CDMA spatial processors to analyze the multi-cell systems.</td>
</tr>
<tr>
<td>CO3</td>
</tr>
<tr>
<td>Learner will be able to analyze the channel models for smart antenna systems.</td>
</tr>
<tr>
<td>CO4</td>
</tr>
<tr>
<td>Learner will be able to understand the environmental parameters for signal processing of smart antenna systems.</td>
</tr>
<tr>
<td>CO5</td>
</tr>
<tr>
<td>Learner will be able to evaluate the requirements for the design and implementation of smart antenna systems</td>
</tr>
</tbody>
</table>

**UNIT I:**

Introduction to Smart Antennas, Need for Smart Antennas, Smart Antenna Configurations.

**UNIT II:**


**UNIT III:**


**UNIT IV:**

UNIT V:

UNIT VI:
Data Rates in MIMO Systems, Single-User Data Rate Limits, Multiple Users Data Rate Limits, Data Rate Limits Within a Cellular System, MIMO in Wireless Local Area Networks, Mobile Stations’ Smart Antennas, Combining Techniques, Selection (Switched) Diversity, Maximal Ratio Combining, Adaptive Beam forming or Optimum Combining, RAKE Receiver Size, Mutual Coupling Effects, Dual-Antenna Performance Improvements, Downlink Capacity Gains

Text Books /Reference:
ELECTIVE-II
REMOTE SENSING SYSTEM

Weekly Teaching Hours
TH: 03  Tut: --

Scheme of Marking
TH: 60  Tests: 20  IA: 20  Total: 100

Course Objectives:

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<table>
<thead>
<tr>
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<tbody>
<tr>
<td>A</td>
<td>To expose the students to the basics of remote sensing system</td>
</tr>
<tr>
<td>B</td>
<td>To provide an in-depth understanding needed to perform image processing using different remote sensing techniques.</td>
</tr>
</tbody>
</table>

Course Outcomes:

| CO1 | The course gives you an insight into remote sensing, both in theory (mathematical and physical background) and in practice (applications and training) |
| CO2 | Learner will be able to understand the information content of remotely sensed data and how to retrieve the information. |
| CO3 | Learner will be able to understand the basics of digital image processing |
| CO4 | Learner will be able to decide which remote sensing techniques suite your specific needs |
| CO5 | Learner will be able to know the current trending topics in the field of remote sensing. |

UNIT I
Introduction, Basic concepts of remote sensing, Energy sources and radiation principles, Energy interactions with atmosphere and earth surface features, Spectral reflectance curves,

UNIT II
Polar orbiting satellites, Spectral, radiometric and spatial resolutions, Multispectral, thermal and hyperspectral sensing.

UNIT III
Digital Image Processing - Image restoration, Image enhancement and Information extraction, Image processing software, Digital Elevation Modeling,

UNIT IV
Sources of digital elevation data, Shuttle Radar Topographic Mission (SRTM) data, DEM for Slope, Aspect, Flow direction, Flow pathways, Flow accumulation, Streams, Catchment area delineation,

UNIT V
Remote sensing applications for watershed management, Rainfallrunoff modeling, Irrigation management, Flood mapping, Drought assessment, Environment and ecology,

UNIT VI
Advanced Topics - Microwave remote sensing, sources of microwave data, Global positioning System (GPS).
Text Books /Reference:

ELECTIVE-II
ADVANCED RADAR SYSTEM

Weekly Teaching Hours
TH : 03   Tut: --

Scheme of Marking
TH :60   Tests : 20   IA: 20   Total : 100

Course Objectives:

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<thead>
<tr>
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<tbody>
<tr>
<td>A</td>
<td>To provide in depth learning of conventional RADAR System.</td>
</tr>
<tr>
<td>B</td>
<td>To overcome the problem faced by conventional RADAR System and implement the modern RADAR system for the welfare of society.</td>
</tr>
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</table>

Course Outcomes:

<p>| | |</p>
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<thead>
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<tbody>
<tr>
<td>CO1</td>
<td>Learner will be able to study historical background of RADAR system.</td>
</tr>
<tr>
<td>CO2</td>
<td>Learner will be able to analyze Doppler radar and MTI: Doppler effect.</td>
</tr>
<tr>
<td>CO3</td>
<td>Learner will be able to learn importance of the ambiguity function and the matched filter.</td>
</tr>
<tr>
<td>CO4</td>
<td>Learner will be able to explore the concepts of Aperture Radar: principles, SAR.</td>
</tr>
<tr>
<td>CO5</td>
<td>Learner will be able to synthesize Kalman filters.</td>
</tr>
<tr>
<td>CO6</td>
<td>Learner will be able to application of Bistatic radar:</td>
</tr>
</tbody>
</table>

UNIT I
Introduction: historical background, radar terminology, radar band designations The radar equation: point targets, radar cross section, distributed targets, propagation, coverage diagrams.

UNIT II
Noise, clutter and detection: theory of detection, sea and land clutter models, CFAR Processing Displays: A,scope, B,scope, PPI, modern displays, Doppler radar and MTI: Doppler effect, delay line cancellers, blind speeds, staggered PRFs, Adaptive Doppler filtering

UNIT III
Pulse Doppler processing and STAP: airborne radar, high, low and medium PRF operation, Space Time Adaptive Processing, Pulse compression: principles, the ambiguity function, the matched filter, chirp waveforms, SAW technology

UNIT IV
Waveform design: nonlinear FM, phase codes, waveform generation and compression FM radar: principles, radar equation, effect of phase and amplitude errors, Synthetic Aperture Radar: principles, SAR processing, autofocus, spotlight mode, airborne and spaceborne systems and applications, interferometry, ISAR.
UNIT V
Tracking radar: conical scan, monopulse, tracker, track while scan, Kalman filters, Avionics and radionavigation: Air Traffic Control, primary and secondary radar, GPS Phased array, radar: phased array principles, array signal processing, multifunction radar, scheduling, Electronic Warfare: ESM, ECM, ECCM; super resolution, IFM, types of jammers, calculation of performance, adaptive arrays, LPI radar.

UNIT VI
Stealth and counter, stealth: stealth techniques for aircraft and other target types, low frequency and UWB radar, Bistatic radar: bistatic geometry, bistatic radar equation, synchronisation, illuminators of opportunity System design examples.

Text Books/Reference:
### ELECTIVE-II

#### OPTICAL FIBER COMMUNICATION

**Weekly Teaching Hours**

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<td><strong>TH</strong></td>
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**Scheme of Marking**

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**Course Objectives:**

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<tbody>
<tr>
<td>A</td>
<td>To expose the students to the basics of signal propagation through optical fibers, fiber impairments, components and devices and system design.</td>
</tr>
<tr>
<td>B</td>
<td>To provide an in-depth understanding needed to perform fiber-optic communication system engineering calculations, identify system tradeoffs, and apply this knowledge to modern fiber optic systems.</td>
</tr>
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</table>

**Course Outcomes:**

<table>
<thead>
<tr>
<th>CO</th>
<th>Description</th>
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</thead>
<tbody>
<tr>
<td>CO1</td>
<td>Learner will be able to recognize and classify the structures of Optical fiber and types.</td>
</tr>
<tr>
<td>CO2</td>
<td>Learner will be able to demonstrate electromagnetic and mathematical analysis of light wave propagation.</td>
</tr>
<tr>
<td>CO3</td>
<td>Learner will be able to analyze fabrication techniques of different optical fibers.</td>
</tr>
<tr>
<td>CO4</td>
<td>Learner will be able to interpret behavior of pulse signal and various loss mechanism.</td>
</tr>
<tr>
<td>CO5</td>
<td>Learner will be able to interpret Dispersion compensation mechanism, Scattering effects and modulation techniques.</td>
</tr>
<tr>
<td>CO6</td>
<td>Learner will be able to interpret working of Fiber based devices.</td>
</tr>
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</table>

**UNIT I**

Introduction and importance of Fiber Optics Technology, Ray analysis of optical fiber: Propagation mechanism of rays in an opticalfiber, Meridional rays, Skew rays, Fiber numerical aperture, dispersion.

**UNIT II**

Electromagnetic (modal) analysis of Step index multimode fibers: Wave equation and boundary conditions, Characteristics equation, TE, TH and Hybrid modes, Weakly guiding approximation, linearly polarized modes, Single mode fiber, V parameter, Power confinement and mode cutoff, Mode field diameter.

**UNIT III**

Graded-index fiber: Modal analysis of graded index fiber, WKB analysis, Optimum profile. Experimental techniques in fiber optics: Fiber fabrication (OVD, VAD, CVD, MCVD,PMCVD etc) and characterization, Splices, Connectors and fiber cable.

**UNIT IV**

Loss mechanism in optical fiber: Absorption loss, scattering loss, bending loss, splice loss.
Pulse propagation, Dispersion and chirping in single mode fibers: Pulse propagation in non-dispersive and dispersive medium, Pulse broadening and chirping, Group and phase velocity, Intermodal and intramodal dispersion, Group velocity (material and waveguide) dispersion, Higher order dispersion, Fiber bandwidth.

**UNIT V**

**UNIT VI**

**Text Books/Reference:**

ELECTIVE-II
STATISTICAL SIGNAL PROCESSING

Weekly Teaching Hours
TH : 03    Tut:  --

Scheme of Marking
TH : 60    Tests : 20    IA: 20    Total : 100

Course Objectives:

<table>
<thead>
<tr>
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<th>Description</th>
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<tbody>
<tr>
<td>A</td>
<td>To provide in-depth understanding of more advanced probability theory, leading into random process theory and focus on discrete time methods.</td>
</tr>
<tr>
<td>B</td>
<td>To provide in-depth understanding of fundamental concepts of statistical signal processing.</td>
</tr>
</tbody>
</table>

Course Outcomes:

| CO1 | Learner will be able to generalize the properties of statistical models in the analysis of Signals using Stochastic processes. |
| CO2 | Learner will be able to compare different Stochastic Processes and Models. |
| CO3 | Learner will be able to demonstrate optimum linear filter algorithms and structures. |
| CO4 | Learner will be able to Differentiate the prominence of various spectral estimation techniques for Achieving higher resolution in the estimation of power spectral density. |
| CO5 | Learner will be able to visualize Least Square Filtering and Computation techniques. |
| CO6 | Learner will be able to interpret adaptive filtering and its applications. |

UNIT I
Introduction

UNIT II

UNIT III

UNIT IV

UNIT V

UNIT VI

Text Books/Reference:
1. S. Haykin, Adaptive Filter Theory, PHI.
ELECTIVE-II
MICROELECTRONICS

Weekly Teaching Hours
TH : 03  Tut: --

Scheme of Marking
TH : 60  Tests : 20  IA: 20  Total : 100

Course Objectives:

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<tbody>
<tr>
<td>A</td>
<td>To provide in-depth understanding and to be able to apply basic concepts of semiconductor physics relevant to devices</td>
</tr>
<tr>
<td>B</td>
<td>To be able to analyze and design microelectronic circuits for linear amplifier and digital applications</td>
</tr>
</tbody>
</table>

Course Outcomes:

| CO1 | Learner will be able to discuss MOS structure in terms of different parameters |
| CO2 | Learner will be able to express different CMOS technologies |
| CO3 | Learner will get knowledge of design rules for the CMOS design |
| CO4 | Learner will be able to understand how devices and integrated circuits are fabricated and describe discuss modern trends in the microelectronics industry |
| CO5 | Learner will be able to determine the frequency range of simple electronic circuits and understand the high frequency limitations of BJTs and MOSFETs |
| CO6 | Learner will be able to design simple devices and circuits to meet stated operating specifications |

UNIT I

UNIT II
CMOS Technologies: Background, Wafer Formation, Photolithography, Well and Channel Formation, Silicon Dioxide (SiO2), Isolation, Gate Oxide, Gate and Source/Drain Formation, Contacts and Metallization, Passivation, Metrology.

UNIT III

**UNIT IV**

**UNIT V**
Static CMOS Logic: Inverter, NAND Gate, Combinational Logic, NOR Gate, Compound Gates, Pass Transistors and Transmission Gates, Tristates, Multiplexers, Latches and Flip-Flops, Circuit Families: Static CMOS, Ratioed Circuits,

**UNIT VI**

**Text Books/Reference:**
COMMUNICATION SKILLS

Weekly Teaching Hours

<table>
<thead>
<tr>
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<th>TH:</th>
<th>Practical:</th>
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Scheme of Marking

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Course Objectives:

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<tbody>
<tr>
<td>A</td>
<td>To become more effective confident speakers and deliver persuasive presentations</td>
</tr>
<tr>
<td>B</td>
<td>To develop greater awareness and sensitivity to some important considerations in interpersonal communication and learn techniques to ensure smoother interpersonal relations</td>
</tr>
</tbody>
</table>

Course Outcomes:

<table>
<thead>
<tr>
<th>CO</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>CO1</td>
<td>Learner will be able to understand the fundamental principles of effective business communication</td>
</tr>
<tr>
<td>CO2</td>
<td>Learner will be able to apply the critical and creative thinking abilities necessary for effective communication in today's business world</td>
</tr>
<tr>
<td>CO3</td>
<td>Learner will be able to organize and express ideas in writing and speaking to produce messages suitably tailored for the topic, objective, audience, communication medium and context</td>
</tr>
<tr>
<td>CO4</td>
<td>Learner will be able to demonstrate clarity, precision, conciseness and coherence in your use of language</td>
</tr>
<tr>
<td>CO5</td>
<td>Learner will be able to become more effective confident speakers and deliver persuasive presentations</td>
</tr>
</tbody>
</table>

UNIT I
Introduction to communication, Necessity of communication skills, Features of good communication, Speaking skills, Feedback & questioning technique, Objectivity in argument

UNIT II
Verbal and Non-verbal Communication, Use and importance of non-verbal communication while using a language, Study of different pictorial expressions of non-verbal communication and their analysis

UNIT III
Academic writing, Different types of academic writing, Writing Assignments and Research Papers, Writing dissertations and project reports

UNIT IV
Presentation Skills: Designing an effective Presentation, Contents, appearance, themes in a presentation; Tone and Language in a presentation, Role and Importance of different tools for effective presentation

UNIT V
Motivation/ Inspiration: Ability to shape and direct working methods according to self-defined criteria; Ability to think for oneself, Apply oneself to a task independently with self-motivation, Motivation techniques: Motivation techniques based on needs and field situations
UNIT VI
Self-management, Self-evaluation, Self-discipline, Self-criticism, Recognition of one’s own limits and deficiencies, dependency etc. Self-awareness, Identifying one’s strengths and weaknesses, Planning & Goal setting, Managing self-emotions, ego, pride leadership & Team dynamics

Text Books/Reference:

**PG Lab-I**

<table>
<thead>
<tr>
<th>Weekly Teaching Hours</th>
<th>TH: --</th>
<th>Practical: 03</th>
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<tbody>
<tr>
<td>Scheme of Marking</td>
<td>TH: --</td>
<td>IA: 25</td>
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<td>PR/OR: 25</td>
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<td>Total: 50</td>
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</table>

Practical’s of the Lab - I shall be based on the courses of first semester. The lab work shall consists of hands on experiments on the different software and hardware platforms related to the syllabus.
**Course Objectives:**

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<tbody>
<tr>
<td>A</td>
<td>To provide in depth understanding of image and video processing.</td>
</tr>
<tr>
<td>B</td>
<td>To perform mathematical analysis of image and video processing and implement it.</td>
</tr>
<tr>
<td>C</td>
<td>To provide solutions to real time problems using knowledge of subject.</td>
</tr>
</tbody>
</table>

**Course Outcomes:**

| CO1 | Learner will be able to basics of correlation and convolution in image processing. |
| CO2 | Learner will be able to understand concept Noise characterization, and Noise restoration filters. |
| CO3 | Learner will be able to estimate different aspects of LZW coding, Transform Coding. |
| CO4 | Learner will be able to start to learn Analog video and Digital Video. |
| CO5 | Learner will be able to estimate 2-D Motion. |
| CO6 | Learner will be able to lean various types of coding. |

**UNIT I**
Light, Brightness adaption and discrimination, Pixels, coordinate conventions, Imaging Geometry, Perspective Projection, Spatial Domain Filtering, sampling and quantization, Intensity transformations, contrast stretching, histogram equalization, Correlation and convolution, Smoothing filters, sharpening filters, gradient and Laplacian

**UNIT II**

**UNIT III**
Encoder-Decoder model, Types of redundancies, Lossy and Lossless compression, Entropy of an information source, Shannon's 1st Theorem, Huffman Coding, Arithmetic Coding, Golomb Coding, LZW coding, Transform Coding, Sub-image size selection, blocking artifacts, DCT implementation using FFT, Run length coding, FAX compression (CCITT Group-3 and Group-4), Symbol-based coding, JBIG-2, Bit-plane encoding, Bit-allocation, Zonal Coding, Threshold Coding, JPEG, Lossless predictive coding, Lossy predictive coding, Motion Compensation
UNIT IV
Basic Steps of Video Processing: Analog video, Digital Video, Time varying Image Formation models: 3D motion models, Geometric Image formation, Photometric Image formation, sampling of video signals, filtering operations

UNIT V
2-D Motion Estimation: Optical flow, general methodologies, pixel based motion estimation, Block matching algorithm, Mesh based motion estimation, global

UNIT VI
Motion Estimation, Region based motion estimation, multi resolution motion estimation. Waveform based coding, Block based transform coding, predictive coding, Application of motion estimation in video coding.

Text Books/Reference:
2. Yao Wang, Joem Ostarmann and Ya quin Zhang, Video Processing and Communication, 1st edition, PHI
INFORMATION THEORY AND CODING

Weekly Teaching Hours
TH : 03  Tut:  01

Scheme of Marking
TH :60  Tests : 20  IA: 20  Total : 100

Course Objectives:

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<table>
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<tbody>
<tr>
<td>A</td>
<td>To provide in-depth understanding of principles and applications of information theory.</td>
</tr>
<tr>
<td>B</td>
<td>To provide in-depth understanding of how information is measured in terms of probability and entropy and how these are used to calculate the capacity of a communication channel.</td>
</tr>
<tr>
<td>C</td>
<td>To provide in-depth understanding of different coding techniques for error detection and correction.</td>
</tr>
</tbody>
</table>

Course Outcomes:

| CO1 | Learner will be able to formulate equations for entropy mutual information and channel capacity for all types of channels. |
| CO2 | Learner will be able to distinguish between different types error correcting codes based on probability of error |
| CO3 | Learner will be able to design a digital communication system by selecting an appropriate error correcting codes for a particular application. |
| CO4 | Learner will be able to explain various methods of generating and detecting different types of error correcting codes |
| CO5 | Learner will be able to formulate the basic equations of linear block codes. |
| CO6 | Learner will be able to compare the performance of digital communication system by evaluating the probability of error for different error correcting codes |

UNIT I

Theory of Probability and Random Processes

Concept of probability, Random variables, Probability models, Statistical averages, Central limit theorem, Correlation, Linear mean square estimation.

UNIT II

Random Processes

Random variable and random process, Power spectral density of a random process, Multiple random processes, Transmission of random processes through linear systems, Band-pass random processes, Optimum filtering.
UNIT III
Noise in Communication Systems
Behavior of analog and digital communication systems in the presence of noise, Sources of noise, Noise representation, Noise filtering, Noise bandwidth, Performance of analog and digital communication systems in the presence of noise.

UNIT IV
Information Theory
Measure of information, Joint entropy and conditional entropy, Relative entropy and mutual information, Markov sources, Source encoding, Shannon-Fano coding and Huffman coding, Shannon's first and second fundamental theorems, Channel capacity theorem.

UNIT V
Error Correcting Codes
Galois fields, Vector spaces and matrices, Block codes, Cyclic codes, Burst-error detecting and correcting codes, Multiple error correcting codes, Convolutional codes, ARQ, Performance of codes, Comparison of coded and un-coded systems.

UNIT VI
Speech Coding
Characteristics of speech signal, Quantization techniques, Frequency domain coding, Vocoder, Linear predictive coders, Codecs for mobile communication, GSM codec, USDC codec, Performance evaluation of speech coders.

Text Books/Reference:
1. B. P. Lathi; Modern Digital and Analog Communication Systems; Oxford Publication.
3. Taub, Schilling; Principles of Communication Engineering (2nd Edition); TMH.
4. Thomas M. Cover, Joy A. Thomas Elements of Information Theory; Wiley Inter-science.
5. R.P.Singh, S.D. Sapre, Communication systems : Analog and Digital; TMH.
**ELECTIVE-III**
**MULTIRATE DIGITAL SIGNAL PROCESSING**

Weekly Teaching Hours  
TH : 03  
Tut: --

Scheme of Marking  
TH :60  
Tests : 20  
IA: 20  
Total : 100

**Course Objectives:**

| A | To master the fundamentals of multirate signal processing and demonstrate the ability to solve problems in sample rate conversion, filter banks, and transmultiplexers. |

**Course Outcomes:**

| CO1 | Learner will be able to develop efficient realizations for upsampling and downsampling of signals using the polyphase decomposition |
| CO2 | Learner will be able to design and implement Finite Impulse Response (FIR) and Infinite Impulse Response (IIR) digital filters to meet specifications |
| CO3 | Learner will be able to design digital filter banks based on the techniques presented |
| CO4 | Learner will be able to analyze fundamental concepts of wavelets. |
| CO5 | Learner will be able to distinguish between wavelets and multirate filter banks, from the point of view of implementation. |

**UNIT I**  
**Fundamentals of Multirate Systems**

Introduction, Basic multirate operations, Interconnection of building blocks, Polyphase representation, Mulstage implementation, Some application of multirate systems, Special filter and filter banks.

**UNIT II**  
**Maximally Decimated Filter Banks**

Introduction, Errors created in the QMF bank, A simple alias free QMF system, Power symmetric QMF banks, M-channel filter banks, Polyphase representation, Perfect reconstruction system, alias free filter banks, Tree structured filter banks, Transmultiplexer.

**UNIT III**  
**Paraunitary Perfect Reconstruction Filter Banks**

Introduction, Lossless transfer matrices, Filter banks properties induced by paraunitariness, Two channel FIR paraunitary QMF banks, Two channel paraunitary QMF lattice, M - channel FIR paraunitary filter banks, Tranform coding and LOT.
UNIT IV
Linear Phase and Cosine Modulated Filter Banks
Introduction, Some necessary conditions, Lattice structure for linear phase FIR PR banks, formal synthesis of linear phase FIR PR QMF Lattice. Pseudo QMF banks, Design of the pseudo QMF bank, Efficient polyphase structure, Cosine modulated perfect reconstruction system.

UNIT V
The Wavelet Transform and its Relation to Multirate Filter Banks
Introduction, Background and outline, Short time fourier transform, The Wavelet transform, DT orthonomal Wavelets, Continuous time orthonormal Wavelet basis.

UNIT VI
Multidimensional, Multivariable and Lossless Systems

Text Books/Reference:
1. P.P.Vaidyanathan , PTR Prentice Hall, Englewood Cliffs , New Jersey, Multirate System and Filter Banks
2. N.J.Fliege , John Wiley & Sons, Multirate Digital Signal Processing
3. Raghuveer Rao, Ajit Bopardikar, Pearson Education Asia, Wavelet Transforms Introduction to Theory and Application
ELECTIVE-III
EMBEDDED SYSTEM DESIGN

Weekly Teaching Hours
TH : 03    Tut: --

Scheme of Marking
TH :60    Tests : 20    IA: 20    Total : 100

Course Objectives:
A To introduce students to the modern embedded systems and to show how to understand and program such systems using a concrete platform built around a modern embedded processor.

Course Outcomes:

| CO1 | Learner will have understanding of fundamental embedded systems design paradigms, architectures, possibilities and challenges, both with respect to software and hardware |
| CO2 | Learner will be able to analyze a wide competence from different areas of technology, especially from computer engineering, study of processor for deep understanding analyze case study of Pentium processor |
| CO3 | Learner will be able to demonstrate architecture of 8051, Instruction set, Addressing modes. Programming 8051 for various applications. Interfacing of LED/LCD, keyboard, stepper motor, ADC/DAC and sensors, RTC, serial communication with micro-controller. |
| CO4 | Learner will be able to analyze deep state-of-the-art theoretical knowledge in the areas of real-time systems, artificial intelligence, learning systems, sensor and measuring systems, and their interdisciplinary nature needed for integrated hardware/software development of embedded systems. |
| CO5 | Learner will have ability to analyze a system both as whole and in the included parts, to understand how these parts interact in the functionality and properties of the system, and understanding and experience of state-of-the-practice industrial embedded systems and intelligent embedded system development. |

UNIT I
Fundamentals of Embedded System
Embedded System overview, Design challenges, Processor Technology, IC Technology, Design Technology.

UNIT II
Embedded System Hardware
Evaluation of Processors, Microprocessor architecture overview- CISC and RISC, Case study of Pentium processor architecture.
UNIT III
Microcontroller Architecture and Interfacing
Architecture of 8051, Instruction set, Addressing modes, Programming Examples. Interfacing of LED/LCD, keyboard, stepper motor, ADC/DAC and sensors, RTC, serial communication with micro-controller.

UNIT IV
Study of semiconductor memory
Memory device characteristics, SRAM, DRAM, SSRAM, SDRAM, RDRAM, FLASH, Smart card memory and interfacing of memory with micro-controller.

UNIT V
Introduction to DSP Processors
Architecture, features, instruction set, typical applications (TMS320XX or ADSP 21010).

UNIT VI
Embedded software and Applications
Applications: Network protocols- TCP/IP, Embedded Ethernet, CANBUS, I2C bus, Mod Bus, Digital Camera.

Text Books/Reference:
4. INTEL Microcontroller Manual
ELECTIVE-III

WIRELESS SENSOR NETWORK DESIGN

Weekly Teaching Hours

TH : 03 Tut: --

Scheme of Marking

TH :60 Tests : 20 IA: 20 Total : 100

Course Objectives:

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<thead>
<tr>
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<tbody>
<tr>
<td>A</td>
<td>To provide in-depth understanding of design and implementation of WSN</td>
</tr>
<tr>
<td>B</td>
<td>To provide ability to formulate and solve problems creatively in the area of WSN</td>
</tr>
<tr>
<td>C</td>
<td>To provide in-depth understanding of various applications of WSN.</td>
</tr>
</tbody>
</table>

Course Outcomes:

| CO1 | Learner will be able to understand the need of WSN and also will analyze the challenges in creating WSN |
| CO2 | Learner will be able to design the architecture of WSN |
| CO3 | Learner will be able analyze the power and security constraints in WSN |
| CO4 | Learner will study different operating system to operate WSN |
| CO5 | Learner will be able to understand the basic functioning of WSN at physical layer |
| CO6 | Learner will be able to understand different protocols at network layer to for multiple channel accessing |

UNIT I


UNIT II

Architectures: Node Architecture, the sensing subsystem, processor subsystem, communication, interface, LMote, XYZ, Hogthrob node architectures

UNIT III


UNIT IV

UNIT V
Physical Layer – Basic Components, Source Encoding, Channel Encoding, Modulation, Signal Propagation

UNIT VI
Medium Access Control – types, protocols, standards and characteristics, challenges, Network Layer- Routing Metrics, different routing techniques.

Text Books/Reference:
ELECTIVE-III
MICROWAVE DESIGN & MEASUREMENT

Weekly Teaching Hours  TH : 03  Tut: --
Scheme of Marking  TH :60  Tests : 20  IA: 20  Total : 100

Course Objectives:

A  To provide in depth understanding of image and video processing.
B  To perform mathematical analysis of image and video processing and implement it.
C  To provide solutions to real time problems using knowledge of subject.

Course Outcomes:

CO1  Learner will be able to start learning of Introduction to Microwaves.
CO2  Learner will be able to Microwave Semiconductor Devices.
CO3  Learner will be able to differentiate Passive and Active microwave Devices.
CO4  Learner will be able to study importance of Impedance transformation, and matching
CO5  Learner will be able to design the antennas for Microwave.
CO6  Learner will be able to learn Modern Trends in Microwaves Engineering.

UNIT I
INTRODUCTION TO MICROWAVES.
History of Microwaves, Microwave Frequency bands. Applications of Microwaves: Civil and Military, Medical, EMI/ EMC, Mathematical model of Microwave Transmission: Concept of Mode, Characteristics of TEM, TE and TM Modes, Losses associated with microwave transmission, Concept of Impedance in Microwave transmission

UNIT II
ANALYSIS OF RF AND MICROWAVE TRANSMISSION LINES
Coaxial Line, Rectangular Waveguide, Circular waveguide, Stripline, Microstrip Line. Microwave Network Analysis: Equivalent Voltages and currents for non-TEM lines, Network parameters for microwave Circuits, Scattering Parameter.

UNIT III
PASSIVE AND ACTIVE MICROWAVE DEVICES:
Microwave Passive components: Directional Coupler, Power Divider, Magic Tee, attenuator, resonator, Transistors, oscillators, mixers. Microwave Semiconductor Devices: Gunn Diodes, IMPATT diodes, Schottky Barrier diodes, PIN diodes. Microwave tubes: Klystron, TWT, Magnetron
UNIT IV
MICROWAVE DESIGN PRINCIPLES

UNIT V
MICROWAVE MEASUREMENTS.
Power, Frequency and impedance measurement at microwave frequency, Network Analyser and measurement of scattering parameters. Spectrum Analyser and measurement of spectrum of a microwave signal. Noise at microwave frequency and measurement of noise figure. Measurement of Microwave antenna parameters, Microwave Systems: Radar Systems, Cellular Phone Satellite Communication. RFID, GPS

UNIT VI
MODERN TRENDS IN MICROWAVES ENGINEERING
Effect of Microwaves on human body, Medical and Civil applications of microwaves, Electromagnetic interference / Electromagnetic Compatibility (EMI / EMC), Monolithic Microwave IC fabrication. RFMEMS for microwave components. Microwave Imaging.

Text Books/Reference:

ELECTIVE-III
ESTIMATION AND DETECTION THEORY

Weekly Teaching Hours
TH: 03    Tut:  --

Scheme of Marking
TH: 60    Tests: 20    IA: 20    Total: 100

Course Objectives:
A  To provide in-depth understanding basics of detection and estimation theory.
B  To be able to design and analyze optimum detection schemes

Course Outcomes:

<table>
<thead>
<tr>
<th>CO1</th>
<th>Learner will have basic knowledge of linear algebra</th>
</tr>
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<tbody>
<tr>
<td>CO2</td>
<td>Learner will be able to Acquire basics of statistical decision theory used for signal detection and estimation.</td>
</tr>
<tr>
<td>CO3</td>
<td>Learner able to understood the detection of deterministic and random signals using statistical models.</td>
</tr>
<tr>
<td>CO4</td>
<td>Learner will be able to the performance of signal parameters using optimal Estimators</td>
</tr>
<tr>
<td>CO5</td>
<td>Learner will be able to understand different estimation schemes such as ML and MMSE estimators</td>
</tr>
</tbody>
</table>

UNIT I
Linear Algebra
Vector space: linear dependence, Basis and dimension, vector subspace, inner product spaces, orthonormal basis and Gram-Schmidt Process of orthogonalisation, computation of linear dependence, linear transformation and matrices, change of basis, orthogonal and unitary transformation, Eigenvalue, Eigen vectors and characteristics equation. Systems theory, stochastic processes, Gauss Markov models, representation of stochastic processes, likelihood and sufficiency.

UNIT II
Binary Decision: Single Observation
Introduction to structure of decision and estimation problems. Maximum Likelihood decision criterion, Neyman-person criterion, Probability of error criterion, Bays risk criterion, Min-Max criterion, problems

UNIT III
Binary Decision: Multiple Observations
Vector observation, The general Gaussian problem, Waveform observations and additive Gaussian noise, problems
UNIT IV
Multiple Decision: Multiple Decision

UNIT V
Composite And Nonparametric Decision Theory
Composite decisions Sign test, Wilason test, problems

UNIT VI
Fundamentals of Estimation

Text Books/Reference:
1. James Melsa and David Cohn, Mc-Graw Hill, Decision and Estimation Theory
**ELECTIVE-IV**

**ADVANCED BIOMEDICAL SIGNAL PROCESSING**

Weekly Teaching Hours  
TH : 03  
Tut: --

Scheme of Marking  
TH : 60  
Tests : 20  
IA : 20  
Total : 100

**Course Objectives:**

| A | To introduce students to the principles of signal processing techniques when applied specifically to biomedical signals |
| B | To provide in depth understanding of methods and tools for extracting information from digitally acquired biomedical signals. |

**Course Outcomes:**

| CO1 | Learner will be able to demonstrate a systematic knowledge of the complex physical and physiological principles that underpin the measurement of biomedical signals. |
| CO2 | Learner will be able to demonstrate an advanced understanding of the principles of digital signal processing. |
| CO3 | Learner will be able to systematically apply advanced methods to extract relevant information from biomedical signal measurements. |
| CO4 | Learner will be able to critically assess the appropriateness of cutting-edge biomedical signal processing techniques for various problems in the field. |
| CO5 | Learner will be able to evaluate the effectiveness of techniques applied to biomedical signals against specific benchmarks. |

**UNIT I**

**Introduction To Biomedical Signals** - Examples of Biomedical signals - ECG, EEG, EMG etc - Tasks in Biomedical Signal Processing - Computer Aided Diagnosis. Origin of bio potentials - Review of linear systems - Fourier Transform and Time Frequency Analysis (Wavelet) of biomedical signals - Processing of Random & Stochastic signals – spectral estimation – Properties and effects of noise in biomedical instruments - Filtering in biomedical instruments

**UNIT II**

UNIT III

UNIT IV
Data Compression: Lossless & Lossy- Heart Rate Variability – Time Domain measures - Heart Rhythm representation - Spectral analysis of heart rate variability - interaction with other physiological signals.

UNIT V
Introduction to EEG: The electroencephalogram - EEG rhythms & waveform - categorization of EEG activity - recording techniques - EEG applications- Epilepsy, sleep disorders, brain computer interface.

UNIT VI
EEG Modeling - linear, stochastic models – Non linear modeling of EEG - artifacts in EEG & their characteristics and processing – Model based spectral analysis - EEG segmentation - Joint Time-Frequency analysis – correlation analysis of EEG channels - coherence analysis of EEG channels.

Text Books/Reference:
2. Willis J Tompkins , Biomedical Signal Processing -, ED, Prentice – Hall, 1993
7. Enderle, “Introduction to Biomedical Engineering,” 2/e, Elsevier, 2005
ELECTIVE-IV
RECONFIGURABLE COMPUTING

Weekly Teaching Hours
TH : 03       Tut: --

Scheme of Marking
TH :60       Tests : 20       IA: 20       Total : 100

Course Objectives:

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<tbody>
<tr>
<td>A</td>
<td>To learn the basics of the field of reconfigurable computing</td>
</tr>
<tr>
<td>B</td>
<td>To learn advance digital design skills by developing a reconfigurable computing application. Learn a hardware design language Chisel - An introduction to research methodology</td>
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</table>

Course Outcomes:

<table>
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<th>Course Outcomes</th>
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<tbody>
<tr>
<td>CO1</td>
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<td>CO4</td>
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<tr>
<td>CO5</td>
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</tbody>
</table>

UNIT I
Types of computing and introduction to RC: General Purpose Computing, Domain-Specific Processors, Application Specific Processors; Reconfigurable Computing, Fields of Application; Reconfigurable Device Characteristics, Configurable, Programmable, and Fixed-Function Devices; General-Purpose Computing, General-Purpose Computing Issues;

UNIT II
Metrics: Density, Diversity, and Capacity; Interconnects, Requirements, Delays in VLSI Structures; Partitioning and Placement

UNIT III
Routing; Computing Elements, LUTs, LUT Mapping, ALU and CLBs; Retiming, Fine-grained & Coarse-grained structures; Multi-context;

UNIT IV
Different architectures for fast computing viz. PDSPs, RALU, VLIW, Vector Processors, Memories, CPLDs, FPGAs, Multi-context FPGA, Partial Reconfigurable Devices; Structure and Composition of Reconfigurable Computing Devices: Interconnect, Instructions, Contexts, Context switching, RP space model;
UNIT V
Reconfigurable devices for Rapid prototyping, Non-frequently reconfigurable systems, Frequently reconfigurable systems; Compile-time reconfiguration, Run-time reconfiguration

UNIT VI
Architectures for Reconfigurable computing: TSFPGA, DPGA, Matrix; Applications of reconfigurable computing: Various hardware implementations of Pattern Matching such as the Sliding Windows Approach, Automaton-Based Text Searching, Video Streaming

Text Books/Reference:
1. Andre Dehon, “Reconfigurable Architectures for General Purpose Computing”.
2. IEEE Journal papers on Reconfigurable Architectures.
ELECTIVE-IV
DIGITAL VLSI DESIGN

Weekly Teaching Hours
TH : 03  Tut: --

Scheme of Marking
TH :60  Tests : 20  IA: 20  Total : 100

Course Objectives:

| A | To understand different abstract levels in Verilog for modeling digital circuits. |
| B | To know the design of MOS memories and the various precautionary methods to be used in their design. |

Course Outcomes:

| CO1 | Learner will be able to understand MOSFET device structures their physical operations, Current voltage characteristics. Fabrication process of MOS device, Making circuit with MOS devices their design equation. designing layout of such circuits, studying pass transistors |
| CO2 | Learner will be able to understand VHDL language for synthesizing Digital Circuits. Digital circuits include asynchronous and synchronous design issues and state machine synthesizing this circuits. Building state machines with Moore and mealy machines. Understanding how to write package, sub program and test benches. |
| CO3 | Learner will be able to understand Programming Technologies, Programmable Logic Block Architectures, Programmable Interconnects, Programmable I/O blocks in FPGAs, Dedicated Specialized Components of FPGAs, and Applications of FPGAs. |
| CO4 | Learner will be able to understand designing of SRAM and DRAM. |
| CO5 | Learner will be able to implement Floor planning concepts, shape functions and floor plan sizing, understanding types of local routing problems Area routing, channel routing, global routing, algorithms for global routing. |
| CO6 | Learner will be able to analyze Need of Design for Testability (DFT), Controllability, predictability, testability, built in Self Test (BIST), Partial and full scan check. Understanding the system which connects host to target and need of boundary scan check, JTAG, Test Access Port (TAP) controller. |

UNIT I
Introduction to VLSI Circuits

UNIT II
Digital Circuit Design using VHDL
Design of sequential circuits, asynchronous and synchronous design issues, state machine modeling (Moore and mealy machines), packages, sub programs, attributes, test benches.

UNIT III
Programmable Logic Devices
Complex Programmable Logic Devices – Architecture of CPLD, Organization of FPGAs, FPGA Programming Technologies, Programmable Logic Block Architectures, Programmable Interconnects, Programmable I/O blocks in FPGAs, Dedicated Specialized Components of FPGAs, and Applications of FPGAs.

UNIT IV
CMOS Subsystem Design
Semiconductor memories, memory chip organization, Random Access Memories (RAM), Static RAM (SRAM), standard architecture, 6T cell, sense amplifier, address decoders, timings. Dynamic RAM (DRAM), different DRAM cells, refresh circuits, timings.

UNIT V
Floor Planning and Placement
Floor planning concepts, shape functions and floor plan sizing, Types of local routing problems Area routing, channel routing, global routing, algorithms for global routing.

UNIT VI
Fault Tolerance and Testability
Types of fault, stuck open, short, stuck at 1, 0 faults, Fault coverage, Need of Design for Testability (DFT), Controllability, predictability, testability, built in Self Test (BIST), Partial and full scan check, Need of boundary scan check, JTAG, Test Access Port (TAP) controller.
Text Books/Reference:

5. Data Sheets of PLDs.
6. Sung-Mo (Steve) Kang, Yusuf Leblebici, CMOS Digital Integrated Circuits, Tata
ELECTIVE-IV
NETWORKS AND SYSTEMS SECURITY

Weekly Teaching Hours
TH : 03  Tut:  --

Scheme of Marking
TH :60  Tests : 20  IA: 20  Total : 100

Course Objectives:

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<tbody>
<tr>
<td>A</td>
<td>To provide in depth understanding of networks and systems security</td>
</tr>
<tr>
<td>B</td>
<td>To correlate the concept of networks and systems security to the concerned field and provide the better security.</td>
</tr>
</tbody>
</table>

Course Outcomes:

<p>| | |</p>
<table>
<thead>
<tr>
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<tbody>
<tr>
<td>CO1</td>
<td>Learner will be able to start learning of networks and systems security</td>
</tr>
<tr>
<td>CO2</td>
<td>Learner will be able to gain the knowledge of Access Control.</td>
</tr>
<tr>
<td>CO3</td>
<td>Learner will be able to concern about Reference Monitors.</td>
</tr>
<tr>
<td>CO4</td>
<td>Learner will be able to study cryptography.</td>
</tr>
<tr>
<td>CO5</td>
<td>Learner will be able to design Network &amp; Software Security.</td>
</tr>
<tr>
<td>CO6</td>
<td>Learner will be able to lean about New Access Control.</td>
</tr>
</tbody>
</table>

UNIT I

UNIT II
Access Control (Spoofing, Password Protection, Single Sign On, Hacking Discussion / BackTrack Demo, Unix Security

UNIT III
Reference Monitors and Security Models (Memory Protection, Access Control, BLP)

UNIT IV

UNIT V
Authentication in Distributed Systems and Key Establishment & Communications, Network & Software Security (Firewalls, Intrusion Detection, Authentication Protocols)

UNIT VI
Mobile Security (GSM, IPv6, WLAN, Bluetooth) & Web Security / New Access Control (Java Security, Cookies, SPKI, Trust, DRM)
Text Books/Reference:


ELECTIVE-IV
MULTICARRIER COMMUNICATION

Weekly Teaching Hours
TH : 03  Tut: --

Scheme of Marking
TH :60  Tests : 20  IA: 20  Total : 100

Course Objectives:

A  To provide in-depth understanding of basic concepts of Multicarrier techniques
B  To make students familiar with modern communication system

Course Outcomes:

<table>
<thead>
<tr>
<th>CO1</th>
<th>Learner will be able to understand practical limits on CDMA &amp; OFDM systems</th>
</tr>
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<tbody>
<tr>
<td>CO2</td>
<td>Learner will be able to understand basic principles of CDMA &amp; OFDM systems</td>
</tr>
<tr>
<td>CO3</td>
<td>Learner will be able to perform analysis of CDMA &amp; OFDM systems</td>
</tr>
<tr>
<td>CO4</td>
<td>Learner will be familiar with other modern communication systems</td>
</tr>
<tr>
<td>CO5</td>
<td>Learner will be able to design CDMA &amp; OFDM systems</td>
</tr>
</tbody>
</table>

UNIT I
Introduction to digital communication: Introduction to digital communication, high rate wireless application, wireless channel fundamentals, digital communication system fundamentals, Multi-Carrier system fundamentals, comparison with single carrier communication system.

UNIT II
System Architecture: Basics of OFDM, FFT implementation, power spectrum, efficiency Impairments of wireless channels to OFDM, signals Performance optimization, clipping in multi-carrier system, channel partitioning, optimization through coding.

UNIT III
Performance, Channel Estimation and Equalization Synchronization, channel estimation & equalization: Timing & frequency offset, synchronization and system architecture, timing and frame synchronization frequency offset estimation. Channel Estimation and equalization Coherent detection, noncoherent detection, performance, channel estimation for MIMO-OFDM

UNIT IV
Channel coding: Need for coding block, coding in OFDM convolution encoding, concatenated coding, coding in OFDM
UNIT V
PAPR Reduction Techniques: Peak power reduction techniques PAPR properties of OFDM signals, PAPR reduction with and without signal distortion PAPR reduction for multi-carrier CDMA

UNIT VI
Applications of multi-carrier communication Coding in OFDM, wireless LAN, digital audio & video broadcasting OFDM based multiple access techniques, mitigation of clipping effects)

Text Books/Reference:
1. Bahai, Saltzberg, Ergen : Multi-carriers Digital communications, Springer
7. OFDM orthogonal frequency Division Multiplexing. Nova Engineering.
ELECTIVE V
INTERNET OF THINGS

Weekly Teaching Hours

<table>
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Scheme of Marking

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<th>TH</th>
<th>Tests</th>
<th>IA</th>
<th>Total</th>
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<tbody>
<tr>
<td>60</td>
<td>20</td>
<td>20</td>
<td>100</td>
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</table>

Course Objectives:

A  Students will be explored to the interconnection and integration of the physical world and the cyber space.

B  To provide ability to design and develop IOT devices.

Course Outcomes:

| CO1  | Learner will be able to understand the meaning of internet in general and IOT in terms of layers, protocols, packets peer to peer communication |
| CO2  | Learner will be able to interpret IOT working at transport layer with the help of various protocols |
| CO3  | Learner will be able to understand IOT concept at data link layer |
| CO4  | Learner will be able to apply the concept of mobile networking to the internet connected devices |
| CO5  | Learner will be able to measure and schedule the performance of networked devices in IOT |
| CO6  | Learner will be able to analyze the challenges involve in developing IOT architecture |

UNIT I
Introduction: What is the Internet of Things: History of IoT, about objects/things in the IoT, Overview and motivations, Examples of applications, IoT definitions, IoT Framework, General observations, ITU-T views, working definitions, and basic nodal capabilities.

UNIT II
Fundamental IoT Mechanisms & Key Technologies: Identification of IoT objects and services, Structural aspects of the IoT, Environment characteristics, Traffic characteristics, scalability, Interoperability, Security and Privacy, Open architecture, Key IoT Technologies, Device Intelligence, Communication capabilities, Mobility support, Device Power, Sensor Technology, RFID technology, Satellite Technology.

UNIT III

UNIT IV
Wireless Technologies For IoT: Layer ½ Connectivity: WPAN Technologies for IoT/M2M, Zigbee /IEEE 802.15.4, Radio Frequency for consumer Electronics (RF4CE), Bluetooth and its low-energy profile, IEEE 802.15.6 WBANS, IEEE 802.15 WPAN TG4j, MBANS, NFC, dedicated short range communication( DSRC) & related protocols. Comparison of WPAN technologies cellular & mobile network technologies for IoT/M2M.
UNIT V

UNIT VI
Internet of Things Application Examples: Smart Metering, advanced metering infrastructure, e-Health/Body area network, City automation, automotive applications. Home automation, smart cards, Tracking, Over-The-Air passive surveillance/Ring of steel, Control application examples.

Text Books/Reference:
2. Daniel Minoli, Building the Internet of Things with IPv6 and MIPv6 The Evolving World of M2M Communications, Wiley Publications
ELECTIVE V
LINEAR ALGEBRA

Weekly Teaching Hours
TH : 03   Tut: --

Scheme of Marking
TH : 60   Tests : 20   IA: 20   Total : 100

Course Objectives:

<table>
<thead>
<tr>
<th>A</th>
<th>To provide in-depth understanding of fundamental concepts of linear algebra</th>
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<tbody>
<tr>
<td>B</td>
<td>To understand the importance of linear algebra and learn its applicability to practical problems</td>
</tr>
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</table>

Course Outcomes:

<table>
<thead>
<tr>
<th>CO1</th>
<th>Learner will be able to solve and analyze linear system of equation</th>
</tr>
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<tbody>
<tr>
<td>CO2</td>
<td>Learner will be able to analyze the direct notations, duality, adjointness, bases, dual bases in linear algebra</td>
</tr>
<tr>
<td>CO3</td>
<td>Learner will be able to understand the concept of Linear transformations and matrices, equivalence, similarity</td>
</tr>
<tr>
<td>CO4</td>
<td>Learner will be able to understand eigen values and eigen vectors using characteristics polynomials</td>
</tr>
<tr>
<td>CO5</td>
<td>Learner will be able to understand the singular value decomposition of the matrix</td>
</tr>
<tr>
<td>CO6</td>
<td>Learner will be able to understand the inverse of matrix</td>
</tr>
</tbody>
</table>

UNIT I

Fields F_q, R, C. Vector Spaces over a field, F_n, F[\omega]=Polynomials in one Variable.

UNIT II

Direct Notations, Ket, bra vector, duality, adjointness, linear transformations, bases, dual bases.

UNIT III

Linear transformations and matrices, equivalence, similarity.

UNIT IV

Eigenvalues, eigenvectors, diagonalization, Jordan canonical form

UNIT V

Bilinear and sesquilinear forms, inner product, orthonormal, bases, orthogonal decomposition, projections

UNIT VI

System of equations, generalized inverses.

Text Books/Reference:

ELECTIVE V
MULTIMEDIA COMMUNICATION

Weekly Teaching Hours           TH : 03  Tut:  --
Scheme of Marking               TH :60  Tests : 20  IA: 20  Total : 100

Course Objectives:

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<tbody>
<tr>
<td>A</td>
<td>To provide in-depth understanding for multimedia communication standards and compression techniques</td>
</tr>
<tr>
<td>B</td>
<td>To provide in-depth understanding for representation of image, video</td>
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Course Outcomes:

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<tbody>
<tr>
<td>CO1</td>
<td>Learner will be able to understand different multimedia communication devices.</td>
</tr>
<tr>
<td>CO2</td>
<td>Learner will be able to analyze different multimedia compression techniques.</td>
</tr>
<tr>
<td>CO3</td>
<td>Learner will be able to analyze fundamental concepts of multimedia building blocks.</td>
</tr>
<tr>
<td>CO4</td>
<td>Learner will be able to demonstrate a diverse portfolio that reflects multimedia aesthetic proficiency.</td>
</tr>
<tr>
<td>CO5</td>
<td>Learner will be able to demonstrate a set of professional skills and competencies in their practice of multimedia communication.</td>
</tr>
</tbody>
</table>

UNIT I
Introduction to Multimedia, Multimedia Information, Multimedia Objects, Multimedia in business and work. Convergence of Computer, Communication and Entertainment products Stages of Multimedia Projects Multimedia hardware, Memory & storage devices, Communication devices, Multimedia software's, presentation tools, tools for object generations, video, sound, image capturing, authoring tools, card and page based authoring tools.

UNIT II
Multimedia Building Blocks: Text, Sound MIDI, Digital Audio, audio file formats, MIDI under windows environment Audio & Video Capture.

UNIT III
UNIT IV
Speech Compression & Synthesis Digital Audio concepts, Sampling Variables, Loss less compression of sound, loss compression & silence compression.

UNIT V
Images Multiple monitors, bitmaps, Vector drawing, lossy graphic compression, image file formatic animations Images standards, JPEG Compression, Zig Zag Coding, Multimedia Database.Content based retrieval for text and images, Video:

UNIT VI
Video representation, Colors, Video Compression, MPEG standards, MHEG Standard Video Streaming on net, Video Conferencing, Multimedia Broadcast Services, Indexing and retrieval of Video Database, recent development in Multimedia.

Text Books/Reference:
3. Mark Nelson, Data Compression Hand Book, BPB.
4. Sleinreitz, Multimedia System, Addison Wesley
ELECTIVE V
RESEARCH METHODOLOGY

Weekly Teaching Hours
TH : 03  Tut: --

Scheme of Marking
TH :60  Tests : 20  IA: 20  Total : 100

Course Objectives:

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<tr>
<td>A</td>
<td>To develop a research orientation among the scholars and to acquaint them with fundamentals of research methods.</td>
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<tr>
<td>B</td>
<td>To develop understanding of the basic framework of research process.</td>
</tr>
<tr>
<td>C</td>
<td>To identify various sources of information for literature review and data collection.</td>
</tr>
<tr>
<td>D</td>
<td>To understand the components of scholarly writing and evaluate its quality.</td>
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</table>

Course Outcomes:

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<tbody>
<tr>
<td>CO1</td>
<td>Learner will be able to understand the meaning, objective, motivation and type of research</td>
</tr>
<tr>
<td>CO2</td>
<td>Learner will be able to formulate their research work with the help of literature review</td>
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<tr>
<td>CO3</td>
<td>Learner will be able to develop an understanding of various research design and techniques</td>
</tr>
<tr>
<td>CO4</td>
<td>Learner will be able to have an overview knowledge of modeling and simulation of research work</td>
</tr>
<tr>
<td>CO5</td>
<td>Learner will be able to collect the statistical data with different methods related to research work</td>
</tr>
<tr>
<td>CO6</td>
<td>Learner will be able to write their own research work with ethics and non-plagiarized way</td>
</tr>
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UNIT I
Introduction: Defining research, Motivation and Objectives, Types of research
Meaning of Research, Objectives of Research, Motivation in Research, Types of Research

UNIT II
Research Formulation: Formulating The research Problem, Literature Review, Development of Working Hypothesis

UNIT III

UNIT IV
Overview of Modeling and Simulation: Classification of models, Development of Models, Experimentation, Simulation.
UNIT V
Statistical Aspects: Methods of Data Collection, Sampling Methods, Statistical analysis, Hypothesis testing.

UNIT VI

Text Books/Reference:
1. J.P. Holman, Experimental Methods for Engineers.
2. C.R. Kothari, Research Methodology, Methods & Techniques.
ELECTIVE-V
WAVELET TRANSFORMS AND ITS APPLICATIONS

Weekly Teaching Hours  TH : 03  Tut:  --
Scheme of Marking  TH :60  Tests : 20  IA: 20  Total : 100

Course Objectives:

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<tbody>
<tr>
<td>A</td>
<td>To provide in-depth understanding of fundamental concepts of Wavelets.</td>
</tr>
<tr>
<td>B</td>
<td>To study wavelet related constructions, its applications in signal processing, communication and sensing.</td>
</tr>
</tbody>
</table>

Course Outcomes:

| CO1 | Learner will be able to understand the meaning of wavelet transform |
| CO2 | Learner will be able to understand the terminologies used in Wavelet transform with its properties |
| CO3 | Learner will be able to demonstrate various filter bank using wavelet transformation |
| CO4 | Learner will be able to understand bases, orthogonal bases in wavelet transform |
| CO5 | Learner will be able to understand different types of wavelet transform |
| CO6 | Learner will be able to design practical system using wavelet transform |

UNIT I
Continuous Wavelet Transform Introduction, Continuous-time wavelets, Definition of the CWT, the VWT as a Correlation, Constant-Factor Filtering Interpretation and Time-Frequency Resolution, the VWT as an Operator, Inverse CWT, Problems.

UNIT II
Introduction to Discrete Wavelet Transform and Orthogonal Wavelet Decomposition: Introduction, Approximation of Vectors in Nested Linear Vector Subspaces, Examples of an MRA, Problems.

UNIT III
MRA, Orthonormal Wavelets, And Their Relationship To Filter Banks: Introduction, Formal Definition of an MRA, Construction of General Orthonormal MRA, a wavelet Basic for the MRA, Problems.

UNIT IV
Digital Filtering Interpretation, Examples of Orthogonal Basic Generating Wavelets, Interpreting Orthonormal MRAs for Discrete-Time signals, Miscellaneous Issues Related to PRQME Filter Banks, generating Scaling Functions and wavelets from Filter Coefficient, Problems.
UNIT V
Wavelet Transform And Data Compression: Introduction, Transform Coding, DTWT for Image Compression, Audio Compression, And Video Coding Using Multiresolution Techniques: a Brief Introduction.

UNIT VI

Text Books/Reference:
2. P.P. Vaidyanathan, PTR Prentice Hall, Englewood Cliffs, New Jersey, Multirate System and Filter Banks
4. Raghuveer Rao, Ajit Bopardikar, Pearson Education Asia, Wavelet Transforms Introduction to Theory and Application
**SEMINAR I**

<table>
<thead>
<tr>
<th>Weekly Teaching Hours</th>
<th>TH:  -</th>
<th>Practical: 04</th>
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<tbody>
<tr>
<td>Scheme of Marking</td>
<td>IA: 50</td>
<td>PR/OR: 50</td>
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</table>

The seminar shall be on the state of the art in the area of the wireless communication and computing and of student’s choice approved by an authority. The student shall submit the duly certified seminar report in standard format, for satisfactory completion of the work duly signed by the concerned guide and head of the Department/Institute.
The mini project shall be based on the recent trends in the industry, research and open problems from the industry and society. This may include mathematical analysis, modelling, simulation, and hardware implementation of the problem identified. The mini project shall be of the student’s choice and approved by the guide. The student has to submit the report of the work carried out in the prescribed format signed by the guide and head of the department/institute.
PROJECT MANAGEMENT AND INTELLECTUAL PROPERTY RIGHTS

Weekly Teaching Hours  TH: -  Practical: -
Scheme of Marking  IA: 50  PR/OR: 50  Total: 100

The Student has to choose this course either from NPTEL/MOOCs/SWAYAM pool. It is mandatory to get the certification of the prescribed course.
Project-I is an integral part of the final project work. In this, the student shall complete the partial work of the project which will consist of problem statement, literature review, project overview, scheme of implementation that may include mathematical model/SRS/UML/ERD/block diagram/PERT chart, and layout and design of the proposed system/work. As a part of the progress report of project-I work, the candidate shall deliver a presentation on progress of the work on the selected dissertation topic.

It is desired to publish the paper on the state of the art on the chosen topic in international conference/journal.

The student shall submit the duly certified progress report of project-I in standard format for satisfactory completion of the work duly signed by the concerned guide and head of the department/institute.
**PROJECT-II**

<table>
<thead>
<tr>
<th>Weekly Teaching Hours</th>
<th>TH: -</th>
<th>Practical: -</th>
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</thead>
<tbody>
<tr>
<td>Scheme of Marking</td>
<td>IA: 100</td>
<td>PR/OR: 100</td>
</tr>
</tbody>
</table>

In Project - II, the student shall complete the remaining part of the project which will consist of the simulation/ analysis/ synthesis/ implementation / fabrication of the proposed project work, work station, conducting experiments and taking results, analysis and validation of results and drawing conclusions.

It is mandatory to publish the paper on the state of the art on the chosen topic in international conference/ journal.

The student shall prepare the duly certified final report of project work in standard format for satisfactory completion of the work duly signed by the concerned guide and head of the department/institute.